NPN - MPS8099; PNP - MPS8599

Amplifier Transistors

Voltage and Current are Negative for PNP Transistors

Features

• These are Pb-Free Devices*

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit | |
|--|-----------------------------------|-------------|-------------|--|
| Collector - Emitter Voltage | V _{CEO} | 80 | Vdc | |
| Collector - Base Voltage | V _{CBO} | 80 | Vdc | |
| Emitter - Base Voltage | V _{EBO} | 6.0 | Vdc | |
| Collector Current – Continuous | Ic | 500 | mAdc | |
| Total Device Dissipation @ T _A = 25°C Derate above 25°C | P _D | 625 5.0 | mW mW/°C | |
| Total Device Dissipation @ T _C = 25°C Derate above 25°C | P _D | 1.5 12 | W mW/°C | |
| Operating and Storage Junction Temperature Range | T _J , T _{stg} | -55 to +150 | °C | |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit | |
|--|-----------------|------|------|--|
| Thermal Resistance, Junction-to-Ambient (Note 1) | $R_{\theta JA}$ | 200 | °C/W | |
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 83.3 | °C/W | |

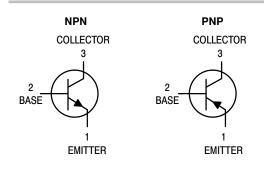
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

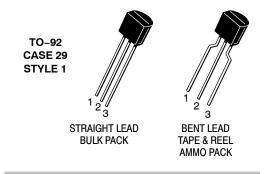
1. $R_{\theta JA}$ is measured with the device soldered into a typical printed circuit board.



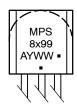
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



x = 0 or 5

A = Assembly Location

Y = Year

WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NPN - MPS8099; PNP - MPS8599

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit | |
|---|----------------------|------------------|---------------|------|--|
| OFF CHARACTERISTICS | | | | | |
| Collector – Emitter Breakdown Voltage (Note 2) (I _C = 10 mAdc, I _B = 0) | V _{(BR)CEO} | 80 | - | Vdc | |
| Collector – Base Breakdown Voltage ($I_C = 100 \mu Adc, I_E = 0$) | V _{(BR)CBO} | 80 | _ | Vdc | |
| Emitter – Base Breakdown Voltage ($I_E = 10 \mu Adc, I_C = 0$) | V _{(BR)EBO} | 6.0 | _ | Vdc | |
| Collector Cutoff Current (V _{CE} = 60 Vdc, I _B = 0) | I _{CES} | - | 0.1 | μAdc | |
| Collector Cutoff Current (V _{CB} = 80 Vdc, I _E = 0) | Ісво | - | 0.1 | μAdc | |
| Emitter Cutoff Current (V _{EB} = 6.0 Vdc, I _C = 0) | I _{EBO} | - | 0.1 | μAdc | |
| ON CHARACTERISTICS (Note 2) | | | | | |
| DC Current Gain | h _{FE} | 100 100 75 | 300 - - | - | |
| Collector – Emitter Saturation Voltage (I_C = 100 mAdc, I_B = 5.0 mAdc) (I_C = 100 mAdc, I_B = 10 mAdc) | V _{CE(sat)} | _ _ | 0.4 0.3 | Vdc | |
| Base–Emitter On Voltage (I _C = 10 mAdc, V _{CE} = 5.0 Vdc) | V _{BE(on)} | 0.6 | 0.8 | Vdc | |
| SMALL-SIGNAL CHARACTERISTICS | | | | | |
| Current – Gain – Bandwidth Product (I _C = 10 mAdc, V _{CE} = 5.0 Vdc, f = 100 MHz) | f _T | 150 | _ | MHz | |
| Output Capacitance ($V_{CB} = 5.0 \text{ Vdc}$, $I_E = 0$, $f = 1.0 \text{ MHz}$) | C _{obo} | _ | 8.0 | pF | |
| Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz) | C _{ibo} | - | 30 | pF | |

^{2.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle = 2.0%.

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|--------------|--------------------|-----------------------|
| MPS8099G | TO-92 (Pb-Free) | 5000 Units / Bulk |
| MPS8099RLRAG | TO-92 (Pb-Free) | 2000 / Tape & Reel |
| MPS8099RLRPG | TO-92 (Pb-Free) | 2000 / Ammo Pack |
| MPS8599RLRAG | TO-92 (Pb-Free) | 2000 / Tape & Reel |
| MPS8599RLRMG | TO-92 (Pb-Free) | 2000 / Ammo Pack |

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

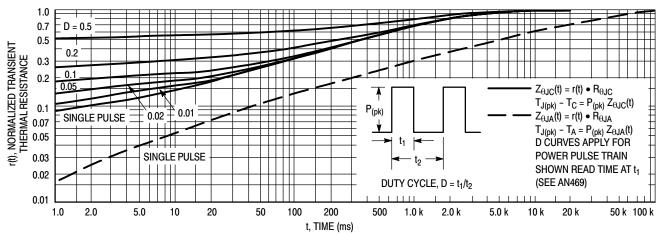
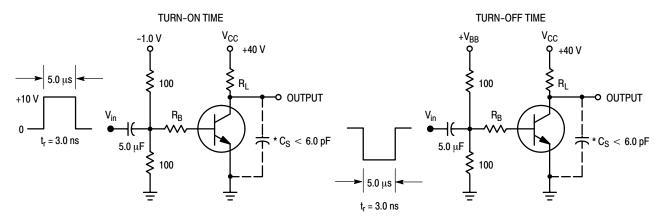


Figure 1. Thermal Response



*Total Shunt Capacitance of Test Jig and Connectors For PNP Test Circuits, Reverse All Voltage Polarities

Figure 2. Switching Time Test Circuits

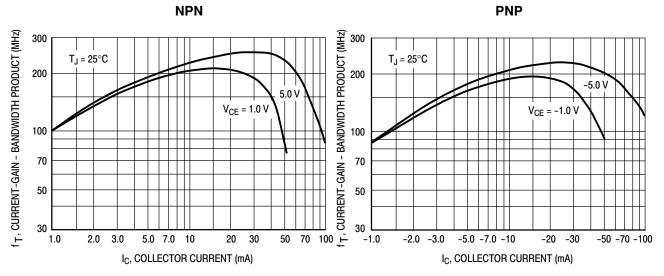


Figure 3. Current-Gain - Bandwidth Product

Figure 4. Current-Gain - Bandwidth Product

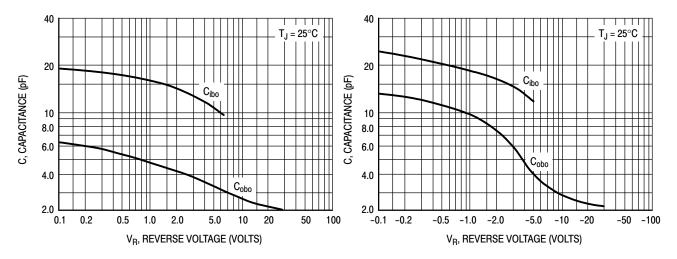


Figure 5. Capacitance

Figure 6. Capacitance

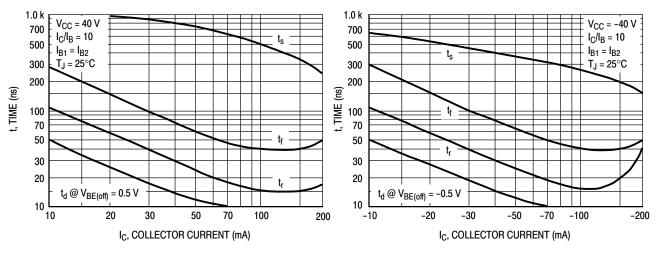


Figure 7. Switching Times

Figure 8. Switching Times

NPN PNP

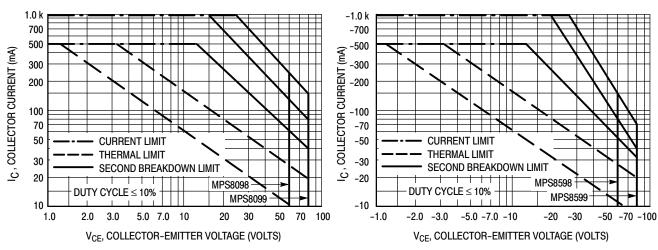


Figure 9. Active-Region Safe Operating Area

Figure 10. Active-Region Safe Operating Area

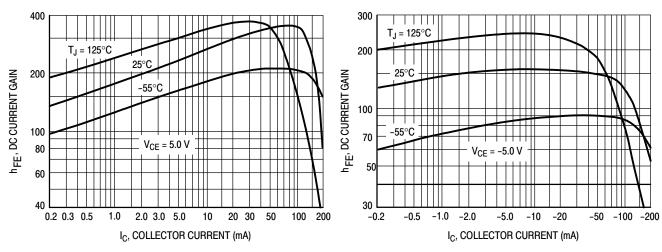


Figure 11. DC Current Gain

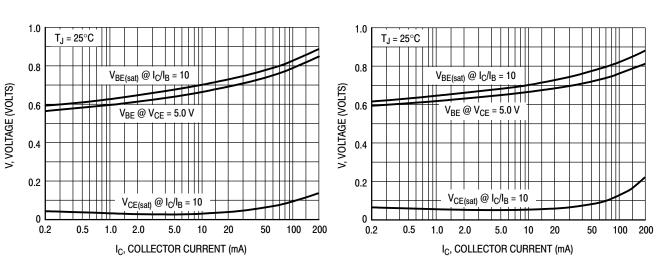


Figure 13. "ON" Voltages

Figure 14. "ON" Voltages

Figure 12. DC Current Gain

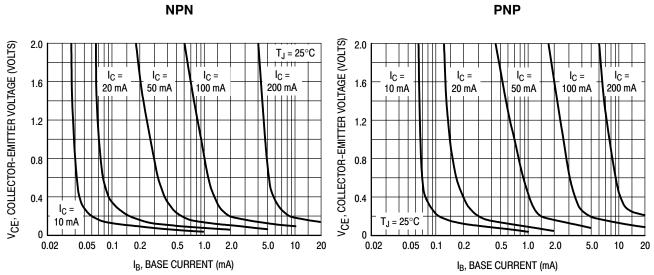


Figure 15. Collector Saturation Region

Figure 16. Collector Saturation Region

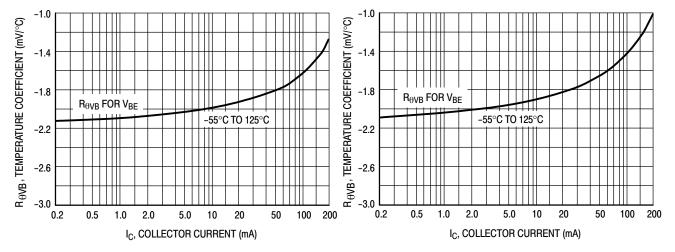


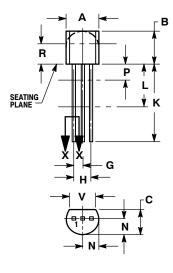
Figure 17. Base-Emitter Temperature Coefficient

Figure 18. Base-Emitter Temperature Coefficient

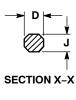
NPN - MPS8099; PNP - MPS8599

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AM**

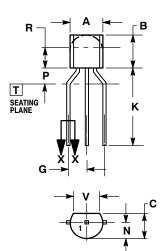


STRAIGHT LEAD **BULK PACK**



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R
- IS UNCONTROLLED.
 LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

| | INCHES | | MILLIN | IETERS |
|-----|--------|-------|--------|--------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.175 | 0.205 | 4.45 | 5.20 |
| В | 0.170 | 0.210 | 4.32 | 5.33 |
| С | 0.125 | 0.165 | 3.18 | 4.19 |
| D | 0.016 | 0.021 | 0.407 | 0.533 |
| G | 0.045 | 0.055 | 1.15 | 1.39 |
| Н | 0.095 | 0.105 | 2.42 | 2.66 |
| J | 0.015 | 0.020 | 0.39 | 0.50 |
| K | 0.500 | | 12.70 | |
| L | 0.250 | | 6.35 | |
| N | 0.080 | 0.105 | 2.04 | 2.66 |
| P | - | 0.100 | | 2.54 |
| R | 0.115 | | 2.93 | |
| v | 0 135 | | 3 43 | |



BENT LEAD TAPE & REEL AMMO PACK



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

- ASME Y14-3M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 CONTOUR OF PACKAGE BEYOND
 DIMENSION R IS UNCONTROLLED.
 LEAD DIMENSION IS UNCONTROLLED IN P
 AND BEYOND DIMENSION K MINIMUM.

| | MILLIMETERS | | |
|-----|-------------|------|--|
| DIM | MIN MAX | | |
| Α | 4.45 | 5.20 | |
| В | 4.32 | 5.33 | |
| С | 3.18 | 4.19 | |
| D | 0.40 | 0.54 | |
| G | 2.40 | 2.80 | |
| J | 0.39 | 0.50 | |
| K | 12.70 | | |
| N | 2.04 | 2.66 | |
| Р | 1.50 | 4.00 | |
| R | 2.93 | | |
| ٧ | 3.43 | | |

STYLE 1: PIN 1. EMITTER

BASE

COLLECTOR

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for sustrain in which the failure of the SCILLC product could create a situation where surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and resanchable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor

P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative