One Watt Darlington Transistor

NPN Silicon

Features

• Pb-Free Package is Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CES}	30	Vdc
Collector - Base Voltage	V _{CBO}	30	Vdc
Emitter - Base Voltage	V _{EBO}	10	Vdc
Collector Current – Continuous	I _C	1.0	Adc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.0 8.0	W mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D 2.5 20		W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

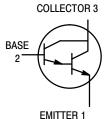
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	125	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	50	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



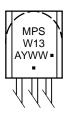
ON Semiconductor®

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MARKING DIAGRAM



MPSW13 = Device Code A = Assembly Location Y = Year

WW = Work Week
■ Pb-Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]	
MPSW13RLRA	TO-92	2,000/Tape & Reel	
MPSW13RLRAG	TO-92 (Pb-Free)	2,000/Tape & Reel	

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage ($I_C = 100 \mu Adc, V_{BE} = 0$)	V _(BR) CES	30	-	Vdc
Collector Cutoff Current $(V_{CB} = 30 \text{ Vdc}, I_E = 0)$	I _{CBO}	_	100	nAdc
Emitter Cutoff Current (V _{EB} = 10 Vdc, I _C = 0)	I _{EBO}	-	100	nAdc
ON CHARACTERISTICS (Note 1)	<u>.</u>			
DC Current Gain $ \begin{aligned} \text{(I}_{\text{C}} &= 10 \text{ mAdc, V}_{\text{CE}} = 5.0 \text{ Vdc)} \\ \text{(I}_{\text{C}} &= 100 \text{ mAdc, V}_{\text{CE}} = 5.0 \text{ Vdc)} \end{aligned} $	h _{FE}	5,000 10,000	_ _	_
Collector-Emitter Saturation Voltage (I _C = 100 mAdc, I _B = 0.1 mAdc)	V _{CE(sat)}	-	1.5	Vdc
Base–Emitter On Voltage ($I_C = 100 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$)	V _{BE(on)}	_	2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS	<u> </u>	•		•
Current–Gain – Bandwidth Product (Note 2) $(I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz})$	f _T	125	-	MHz

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

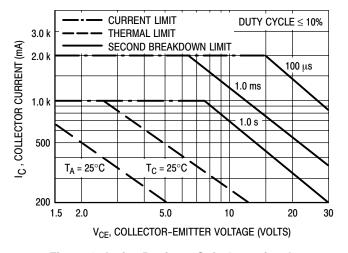


Figure 1. Active Region - Safe Operating Area

^{2.} $f_T = |h_{fe}| \cdot f_{test}$.

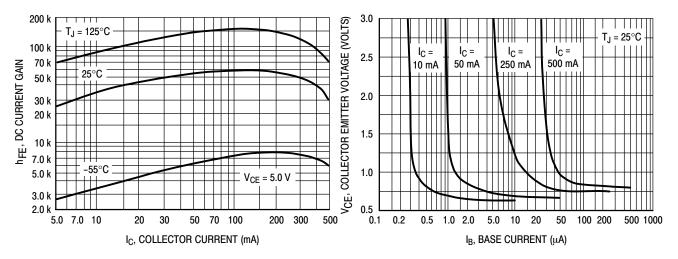


Figure 2. DC Current Gain

Figure 3. Collector Saturation Region

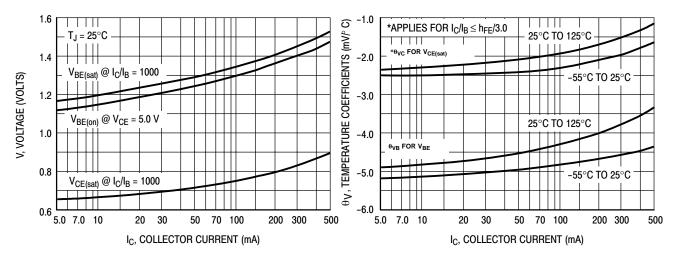


Figure 4. "ON" Voltages

Figure 5. Temperature Coefficients

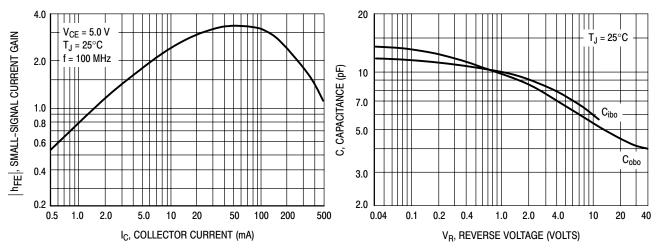
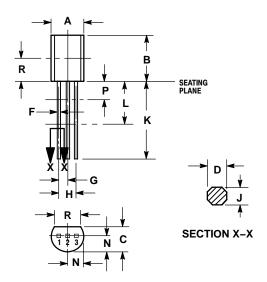


Figure 6. High Frequency Current Gain

Figure 7. Capacitance

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-10 ISSUE AL



NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- 4. DIMENSION F APPLIES BETWEEN P AND L
 DIMENSIONS D AND J APPLY BETWEEN L AND K
 MIMIMUM. LEAD DIMENSION IS UNCONTROLLED
 IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.44	5.21	
В	0.290	0.310	7.37	7.87	
С	0.125	0.165	3.18	4.19	
D	0.018	0.021	0.457	0.533	
F	0.016	0.019	0.407	0.482	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.018	0.024	0.46	0.61	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
P		0.100		2.54	
R	0.135		3 43		

STYLE 1: PIN 1. EMITTER

2. BASE

3. COLLECTOR

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