The RF Line NPN Silicon RF Power Transistor

... designed primarily for wideband large-signal output amplifier stages in the 30-200 MHz frequency range.

- Guaranteed Performance at 150 MHz, 28 Vdc Output Power = 80 Watts Minimum Gain = 10 dB
- Built-In Matching Network for Broadband Operation
- 100% Tested for Load Mismatch at all Phase Angles with 30:1 VSWR
- Gold Metallization System for High Reliability Applications

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|---|------------------|-------------|---------------|
| Collector–Emitter Voltage | V _{CEO} | 35 | Vdc |
| Collector–Base Voltage | V _{CBO} | 65 | Vdc |
| Emitter-Base Voltage | VEBO | 4.0 | Vdc |
| Collector Current — Continuous Peak | IC | 9.0 13.5 | Adc |
| Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C | PD | 220 1.26 | Watts W/°C |
| Storage Temperature Range | T _{stg} | -65 to +150 | °C |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Мах | Unit |
|--------------------------------------|-------------------|-----|------|
| Thermal Resistance, Junction to Case | R _θ JC | 0.8 | °C/W |

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted.)

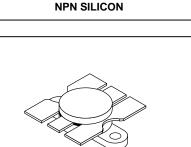
| Characteristic | Symbol | Min | Тур | Max | Unit |
|---|-----------------|-----|-----|-----|------|
| OFF CHARACTERISTICS | | | | | |
| Collector–Emitter Breakdown Voltage $(I_C = 50 \text{ mAdc}, I_B = 0)$ | V(BR)CEO | 35 | - | - | Vdc |
| Collector–Emitter Breakdown Voltage $(I_C = 50 \text{ mAdc}, V_{BE} = 0)$ | V(BR)CES | 65 | - | - | Vdc |
| Collector–Base Breakdown Voltage $(I_C = 50 \text{ mAdc}, I_E = 0)$ | V(BR)CBO | 65 | - | - | Vdc |
| Emitter–Base Breakdown Voltage ($I_E = 5.0 \text{ mAdc}, I_C = 0$) | V(BR)EBO | 4.0 | - | - | Vdc |
| Collector Cutoff Current ($V_{CB} = 30 \text{ Vdc}, I_E = 0$) | ІСВО | _ | - | 5.0 | mAdc |
| ON CHARACTERISTICS | | | | | |
| DC Current Gain (I _C = 4.0 Adc, V _{CE} = 5.0 Vdc) | hFE | 10 | - | 80 | - |
| DYNAMIC CHARACTERISTICS | · | | - | - | - |
| Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz) | C _{ob} | _ | 100 | 130 | pF |

NOTE:

(continued)

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.





MRF316

80 W, 3.0-200 MHz

CONTROLLED "Q" BROADBAND RF POWER

TRANSISTOR

CASE 316-01, STYLE 1

ELECTRICAL CHARACTERISTICS — continued ($T_C = 25^{\circ}C$ unless otherwise noted.)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|---|-----------------|--------------------------------|-----|-----|------|
| NARROW BAND FUNCTIONAL TESTS (Figure 1) | | | | | |
| Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 80 W, f = 150 MHz) | G _{PE} | 10 | 13 | — | dB |
| Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 80 W, f = 150 MHz) | η | 55 | | — | % |
| Load Mismatch (V _{CC} = 28 Vdc, P _{OUt} = 80 W CW, f = 150 MHz, VSWR = 30:1 all phase angles) | Ψ | No Degradation in Output Power | | | |

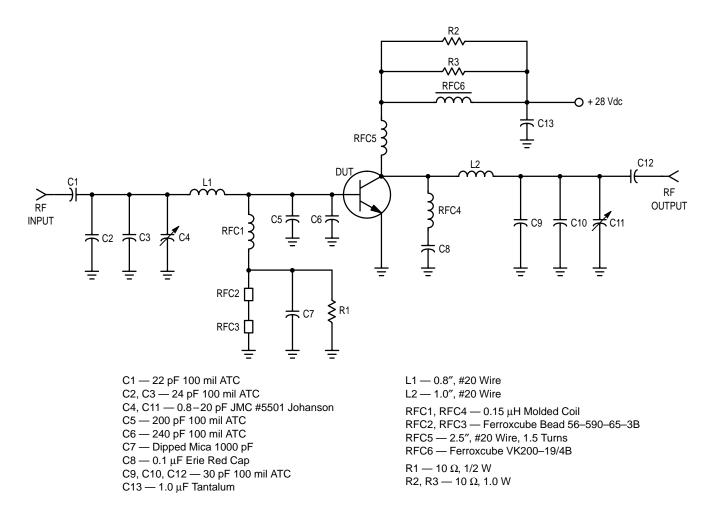


Figure 1. 150 MHz Test Amplifier

TYPICAL PERFORMANCE CURVES

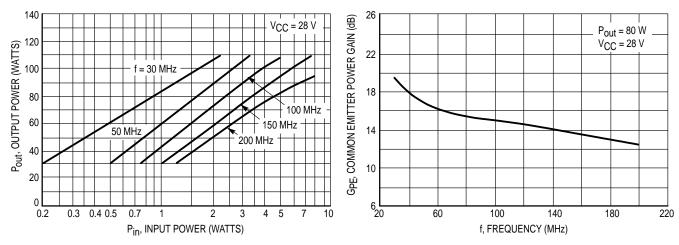
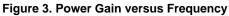


Figure 2. Output Power versus Input Power



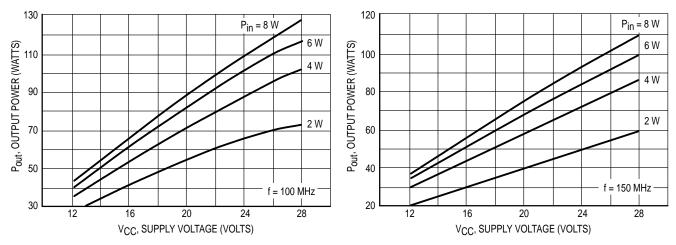


Figure 4. Output Power versus Supply Voltage

Figure 5. Output Power versus Supply Voltage

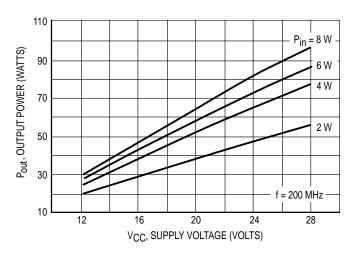
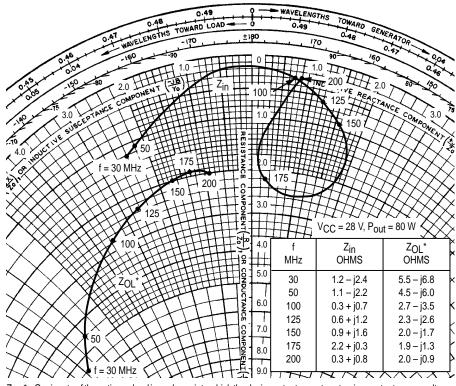


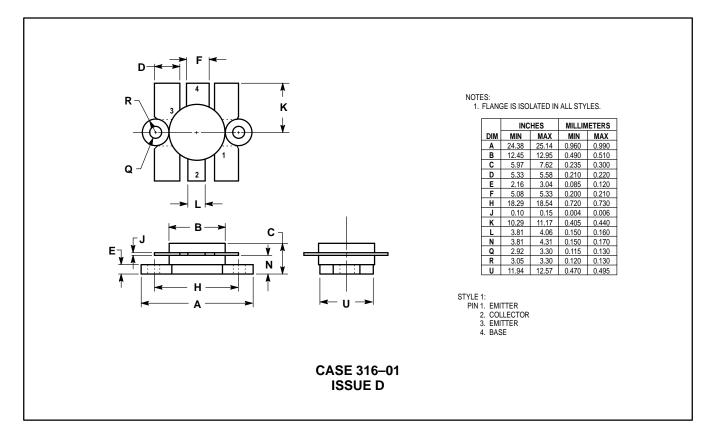
Figure 6. Output Power versus Supply Voltage



 Z_{OL}^* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

Figure 7. Series Equivalent Input–Output Impedance

PACKAGE DIMENSIONS



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