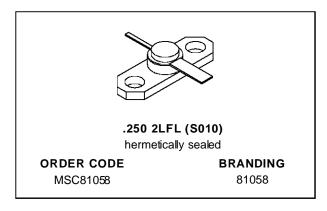


MSC81058

RF & MICROWAVE TRANSISTORS GENERAL PURPOSE AMPLIFIER APPLICATIONS

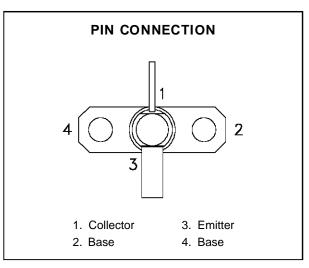
- EMITTER BALLASTED
- REFRACTORY/GOLD METALLIZATION
- VSWR CAPABILITY ∞:1 @ RATED CONDITIONS
- HERMETIC STRIPAC® PACKAGE
- P_{OUT} = 10 W MIN. WITH 10 dB GAIN @ 1 GHz



DESCRIPTION

The MSC81058 is a common base hermetically sealed silicon NPN microwave transistor utilizing a fishbone, emitter ballasted geometry with a refractory/gold metallization system. This device is capable of withstanding infinite load VSWR at any phase angle under rated conditions.

The MSC81058 is designed for Class C amplifier applications in the 0.4 - 1.2 GHz frequency range.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
P _{DISS}	Power Dissipation*	29	W
Ic	Device Current*	1.0	А
Vcc	Collector-Supply Voltage*	35	V
TJ	Junction Temperature	200	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

R _{TH(j-c)}	Junction-Case Thermal Resistance*	6.0	°C/W

^{*}Applies only to rated RF amplifier operation

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ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

STATIC

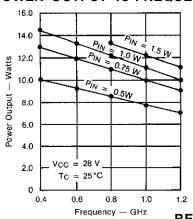
Cumb al		Tool Conditions		Value			I I m i t
Symbol		Test Conditions		Min.	Тур.	Max.	Unit
ВУсво	I _C = 1mA	$I_E = 0mA$		45	_	_	V
BV _{EBO}	I _E = 1mA	$I_C = 0mA$		3.5	_	_	V
BV _{CER}	IC = 10mA	$R_{BE} = 10\Omega$		45	_	_	V
Ісво	V _{CB} = 28V			_	_	2.5	mA
hFE	V _{CE} = 5V	$I_C = 500 \text{mA}$		15	_	120	_

DYNAMIC

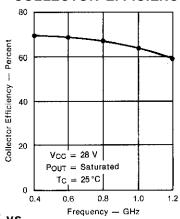
Symbol		Test Conditions		Value		Unit	
Syllibol		rest conditions			Тур.	Max.	Oiiit
Роит	f = 1.0 GHz	$P_{IN} = 1.0 W$	$V_{CC} = 28 V$	10	11		W
ης	f = 1.0 GHz	$P_{IN} = 1.0 W$	$V_{CC} = 28 \text{ V}$	60	64	_	%
G _P	f = 1.0 GHz	$P_{IN} = 1.0 \text{ W}$	$V_{CC} = 28 \text{ V}$	10	10.4	_	dB
СОВ	f = 1 MHz	V _{CB} = 28 V		_	_	10	pF

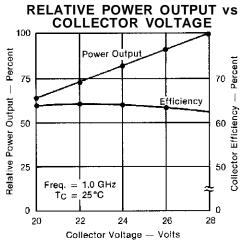
TYPICAL PERFORMANCE

POWER OUTPUT vs FREQUENCY



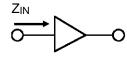
FREQUENCY vs COLLECTOR EFFICIENCY



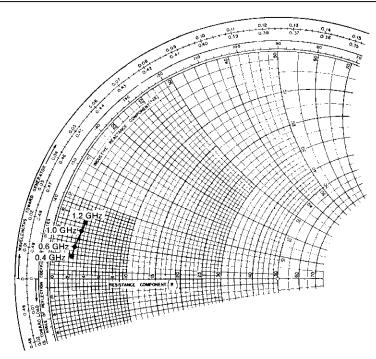


IMPEDANCE DATA

TYPICAL INPUT IMPEDANCE

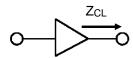


 $P_{IN} = 1.0 \text{ W}$ $V_{CC} = 28 \text{ V}$ Normalized to 50 ohms

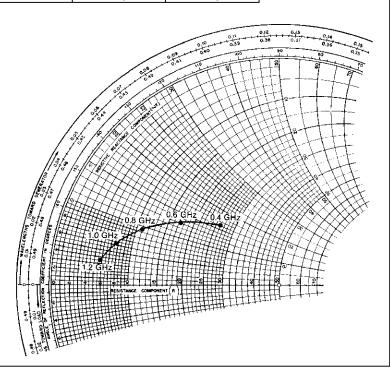


FREQ.	Z _{IN} (Ω)	Z _{CL} (Ω)
0.4 GHz	2.3 + j 2.7	26.0 + j 16.0
0.6 GHz	2.5 + j 4.0	17.2 + j 13.0
0.8 GHz	2.8 + j 5.0	11.0 + j 9.5
1.0 GHz	3.0 + j 6.0	7.7 + j 6.3
1.2 GHz	3.3 + j 7.2	5.8 + j 3.5

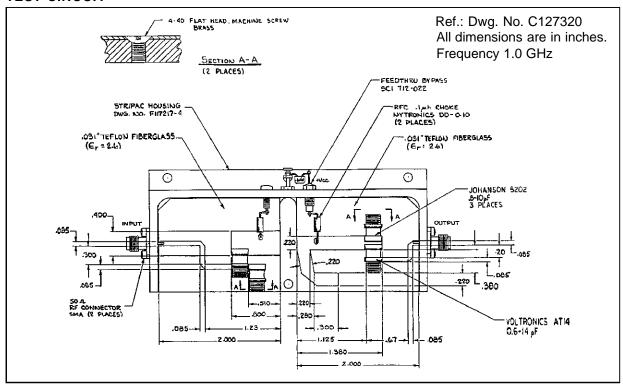
TYPICAL COLLECTOR LOAD IMPEDANCE



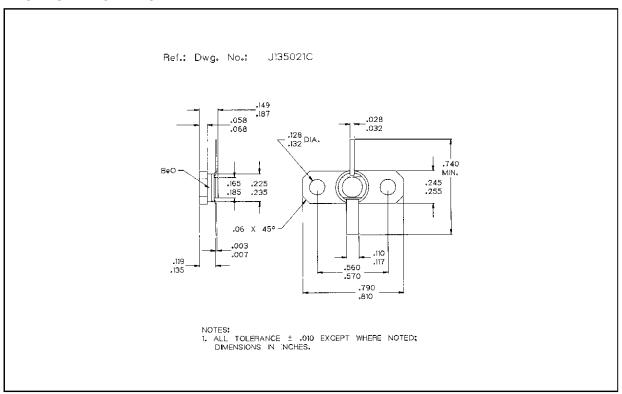
 $P_{OUT} = Saturated$ $V_{CC} = 28 \text{ V}$ Normalized to 50 ohms



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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