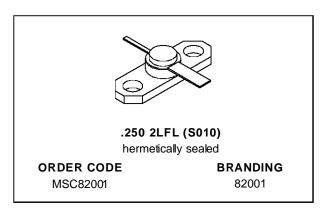


MSC82001

RF & MICROWAVE TRANSISTORS GENERAL PURPOSE AMPLIFIER APPLICATIONS

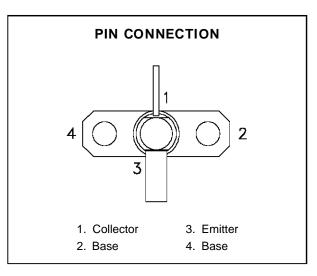
- EMITTER BALLASTED
- REFRACTORY/GOLD METALLIZATION
- VSWR CAPABILITY ∞:1 @ RATED CONDITIONS
- HERMETIC STRIPAC® PACKAGE
- P_{OUT} = 1.0 W MIN. WITH 7.0 dB GAIN

 @ 2.0 GHz



DESCRIPTION

The MSC82001 is a common base hermetically sealed silicon NPN microwave transistor utilizing a fishbone emitter ballasted geometry with a refractory/gold metallization system. This device is capable of withstanding an infinite load VSWR at any phase angle under rated rated conditions. The MSC82001 was designed for Class C amplifier applications in the 1.0 - 2.0 GHz frequency range.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
Poiss	Power Dissipation*	7.0	W
Ic	Device Current*	200	mA
Vcc	Collector-Supply Voltage*	35	V
TJ	Junction Temperature	200	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

R _{TH(j-c)} Junction-Case Thermal Resistance*	20	°C/W
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^{*}Applies only to rated RF amplifier operation

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ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

STATIC

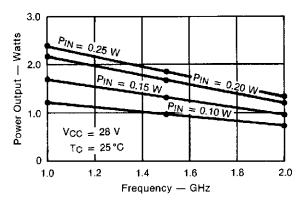
Symbol	Took Conditions	Value			11!4		
	Test Conditions		Min.	Тур.	Max.	Unit	
ВУсво	I _C = 1mA	$I_E = 0mA$		45	_	_	V
BV _{EBO}	I _E = 1mA	$I_C = 0mA$		3.5	_	_	V
BV _{CER}	IC = 5mA	$R_{BE} = 10\Omega$		45	_	_	V
Ісво	V _{CB} = 28V			_	_	0.5	mA
hFE	V _{CE} = 5V	$I_C = 100 \text{mA}$		15	_	120	_

DYNAMIC

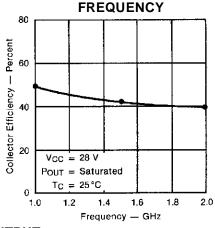
Symbol	Test Conditions		Value			Unit	
Syllibol		rest Conditions		Min.	Тур.	Max.	Oiiit
Роит	f = 2.0 GHz	$P_{IN} = 0.2 \text{ W}$	$V_{CC} = 28 V$	1.0	1.2		W
ης	f = 2.0 GHz	$P_{IN} = 0.2 W$	$V_{CC} = 28 \text{ V}$	35	40	_	%
G _P	f = 2.0 GHz	$P_{IN} = 0.2 \text{ W}$	$V_{CC} = 28 \text{ V}$	7.0	7.8	_	dB
СОВ	f = 1 MHz	V _{CB} = 28 V		_	_	3.2	pF

TYPICAL PERFORMANCE

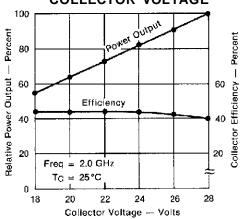
POWER OUTPUT vs FREQUENCY



COLLECTOR EFFICIENCY vs



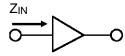
RELATIVE POWER OUTPUT vs COLLECTOR VOLTAGE



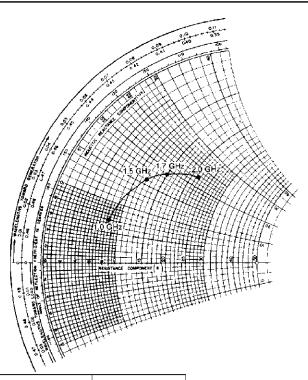


IMPEDANCE DATA

TYPICAL INPUT IMPEDANCE

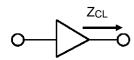


$$\begin{split} P_{IN} &= 0.2 \ W \\ V_{CC} &= 28 \ V \\ Normalized \ to \ 50 \ ohms \end{split}$$

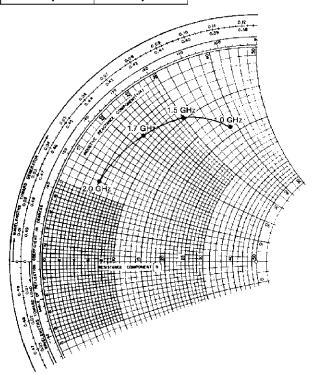


FREQ.	Z _{IN} (Ω)	Z _{CL} (Ω)
1.0 GHz	8.3 + j 7.0	18.0 + j 38.0
1.5 GHz	12.0 + j 16.0	9.6 + j 30.0
1.7 GHz	15.0 + j 14.0	7.0 + j 22.0
2.0 GHz	21.5 + j 22.5	5.0 + j 12.0

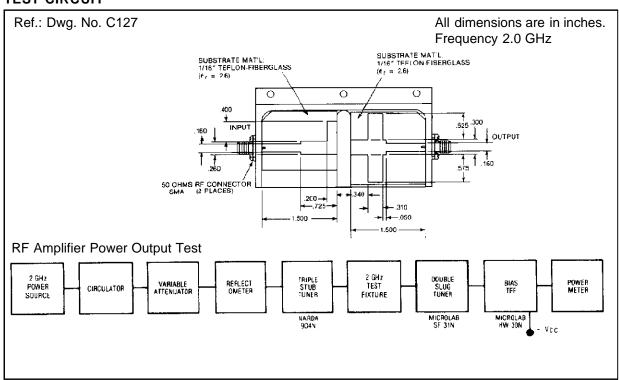
TYPICAL COLLECTOR LOAD IMPEDANCE



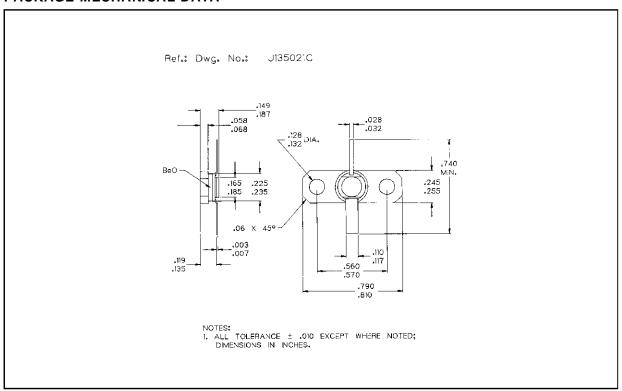
 $P_{OUT} = Saturated$ $V_{CC} = 28 V$ Normalized to 50 ohms



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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