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**MTM55N08
MTM55N10
MTM60N05
MTM60N06**

**55 and 60 AMPERE
N-CHANNEL TMOS
POWER FETs**
 $r_{DS(on)} = 0.04 \text{ OHM}$
80 and 100 VOLTS
 $r_{DS(on)} = 0.028 \text{ OHM}$
50 and 60 VOLTS

MAXIMUM RATINGS

Rating	Symbol	MTM				Unit
		60N05	60N06	55N08	55N10	
Drain-Source Voltage	V_{DSS}	50	60	80	100	Vdc
Drain-Gate Voltage ($R_{GS} = 1 \text{ M}\Omega$)	V_{DGR}	50	60	80	100	Vdc
Gate-Source Voltage Continuous Non-repetitive ($t_p \leq 50 \mu\text{s}$)	V_{GS} V_{GSM}			± 20	± 40	Vdc Vpk
Drain Current Continuous Pulsed	I_D I_{DM}		60 300		55 275	Adc
Total Power Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D			250 2		Watts W/ $^\circ\text{C}$
Operating and Storage Temperature Range	T_J, T_{stg}			-65 to 150		$^\circ\text{C}$

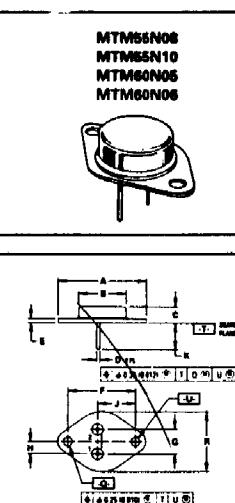
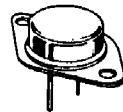
THERMAL CHARACTERISTICS

Thermal Resistance Junction to Case	$R_{\theta JC}$	0.5	$^\circ\text{C/W}$
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T_L	275	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Drain-Source Breakdown Voltage ($V_{GS} = 0, I_D = 5.0 \text{ mA}$)	$V_{BR(DSS)}$	50 60 80 100	—	Vdc
MTM60N05 MTM60N06 MTM55N08 MTM55N10				
Zero Gate Voltage Drain Current ($V_{DS} = \text{Rated } V_{DS}, V_{GS} = 0$) $T_C = 125^\circ\text{C}$	I_{DSS}	— —	10 100	$\mu\text{A/dc}$
Gate-Body Leakage Current ($V_{GS} = 20 \text{ Vdc}, V_{DS} = 0$)	I_{GSS}	—	100	nAdc
ON CHARACTERISTICS*				
Gate Threshold Voltage ($V_{DS} = V_{GS}, I_D = 1 \text{ mA}, V_{DS} = V_{GS}$) $T_J = 100^\circ\text{C}$	$V_{GS(th)}$	2 1.5	4.5 4	Vdc
Static Drain-Source On-Resistance ($V_{GS} = 10 \text{ Vdc}, I_D = 30 \text{ Adc}$) ($V_{GS} = 10 \text{ Vdc}, I_D = 27.5 \text{ Adc}$)	$r_{DS(on)}$	— —	0.028 0.04	Ohm
MTM60N05/MTM60N06 MTM55N08/MTM55N10				
Drain-Source On-Voltage ($V_{GS} = 10 \text{ V}$) ($I_D = 60 \text{ Adc}$) ($I_D = 30 \text{ Adc}, T_J = 100^\circ\text{C}$) ($I_D = 55 \text{ Adc}$) ($I_D = 27.5 \text{ Adc}, T_C = 100^\circ\text{C}$)	$V_{DS(on)}$	— — — —	1.98 1.68 2.6 2.2	Vdc
MTM60N05/MTM60N06 MTM60N05/MTM60N06 MTM55N08/MTM55N10 MTM55N08/MTM55N10				
Forward Transconductance ($V_{DS} = 15 \text{ V}, I_D = 30 \text{ A}$) ($V_{DS} = 15 \text{ V}, I_D = 27.5 \text{ A}$)	g_{FS}	10 10	—	mhos
MTM60N05/MTM60N06 MTM55N08/MTM55N10				

**MTM55N08
MTM55N10
MTM60N05
MTM60N06**



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

STYLE 3
PIN 1: GATE
2: SOURCE
CASE DRAIN

	MILLIMETERS	INCHES
A	20.30	.7927
B	19.31	.7598
C	2.00	.0787
D	1.65	.0649
E	1.53	.0597
F	30.15 BSC	1.187 BSC
G	10.32 BSC	0.403 BSC
H	5.46 BSC	0.215 BSC
J	16.00 BSC	0.629 BSC
K	11.18	.439
L	0.449	.0175
M	2.04	.0797
N	25.15	.989
O	2.04	.0797
P	2.04	.0797
Q	4.19	.164
R	2.04	.0797
S	4.19	.164
U	2.04	.0797

TO-204AE

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Quality Semi-Conductors

