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MTP12N20

Power Field Effect Transistor N-Channel Enhancement-Mode Silicon Gate

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	VDSS	200	Vdc
Drain-Gate Voltage (R _{GS} = 1 M Ω)	VDGR	200	Vdc
Gate-Source Voltage — Continuous — Non-repetitive (tp ≤ 50 µs)	V _{GS} V _{GSM}	± 20 ± 40	Vdc Vpk
Drain Current — Continuous — Pulsed	ID IDM	12 40	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	PD	100 0.8	Watts W/°C
Operating and Storage Temperature Range	Tj, Tstg	-65 to 150	°C
HERMAL CHARACTERISTICS			
Thermal Resistance Junction to Case	R _{ØJC}	1.25	°C/W
Junction to Ambient TO-220	R _{ØJA}	62.5	7
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	ŤL	260	°C



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NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

Quality Semi-Conductors

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
FF CHARACTERISTICS			•···		
Drain-Source Breakdown Voltage {VGS = 0, Ip = 0.25 mA}		V(BR)DSS	200		Vdc
Zero Gate Voltage Drain Current (VDS = Rated VDSS, VGS - 0) (VDS = Rated VDSS, VGS - 0, TJ = 125°C)		DSS	-	10 100	μAdc
Gate-Body Leakage Current, Forward (VGSF = 20 Vdc, VDS = 0)		IGSSF		100	nAdc
Gate-Body Leakage Current, Reverse (VGSR = 20 Vdc, VDS = 0)		IGSSR		100	nAdc
DN CHARACTERISTICS*	-				
Gate Threshold Voltage [VDS = VGS, ID = 1 mA] TJ = 100°C		VGS(th)	2 1.5	4.5 4	Vdc
Static Drain-Source On-Resistance (VGS = 10 Vdc, ID = 6 Adc)		RDS(on)	_	0.35	Ohm
Drain-Source On-Voltage (V _{GS} = 10 (I _D = 12 Adc) (I _D = 6 Adc, T _J = 100°C)	V)	VDS(on)		5 4.2	Vdc
Forward Transconductance (VDS -	15 V, I _D = 6 A)	9FS	4.5	_	mhos
YNAMIC CHARACTERISTICS					•
Input Capacitance	(V _{DS} - 25 V, V _{GS} - 0, f - 1 MHz) See Figure 11	Ciss	<u> </u>	1000	pF
Output Capacitance		Coss		400	
Reverse Transfer Capacitance		Crss	-	100	
WITCHING CHARACTERISTICS* (TJ -	· 100°C)				
Turn-On Delay Time		td(on)	-	50	ns
Rise Time	(V _{DD} = 25 V, I <u>D</u> ≕ 0.5 Rated I _D	tr	-	250	
Turn-Off Delay Time	R _{gen} ≕ 50 ohms) See Figures 9, 13 and 14	td(off)	—	100	
Fall Time	_	tf		120	
Total Gate Charge	(VDS - 0.8 Rated VDSS,	αg	24 (Typ)	50	nC
Gate-Source Charge	Ip = Rated Ip, VGS = 10 V)	0 _{gs}	13 (Typ)	_	
Gate-Drain Charge	See Figure 12	0 _{gd}	11 (Typ)	—	
OURCE DRAIN DIODE CHARACTERIS	TICS•				,
Forward On-Voltage	(IS = Rated ID VGS = 0)	V _{SD}	1.5 (Typ)	3	Vdc
Forward Turn-On Time		ton	Limited by stray inductance		
Reverse Recovery Time		t _{rr}	300 (Typ)	_	ns
NTERNAL PACKAGE INDUCTANCE					
Internal Drain Inductance (Measured from the contact screw (Measured from the drain lead 0.2)		La	3.5 (Тур) 4.5 (Тур)	_	nH
Internal Source Inductance	25" from package to source bond pad.	L _S	7.5 (Typ)	_	

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