

MOS FIELD EFFECT TRANSISTOR

NP48N055CHE,NP48N055DHE,NP48N055EHE,NP48N055KHE

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

These products are N-channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Channel temperature 175 degree rated
- Super low on-state resistance RDS(on) = 17 m Ω MAX. (Vgs = 10 V, ID = 24 A)
- Low Ciss: Ciss = 1600 pF TYP.
- Built-in gate protection diode

ORDERING INFORMATION

	PART NUMBER	PACKAGE
	NP48N055CHE	TO-220AB
	NP48N055DHE	TO-262
	NP48N055EHE	TO-263 (MP-25ZJ)
t	NP48N055KHE	TO-263 (MP-25ZK)

(TO-220AB)

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	55	V
Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±48	Α
Drain Current (pulse) Note1	D(pulse)	±140	Α
Total Power Dissipation (T _A = 25°C)	Рт	1.8	W
Total Power Dissipation (Tc = 25°C)	Рт	85	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	-55 to +175	°C
Single Avalanche Current Note2	las	48/28/10	Α
Single Avalanche Energy Note2	Eas	2.3/78/100	mJ

Notes 1. PW \leq 10 μ s, Duty cycle \leq 1%

2. Starting Tch = 25°C, Rg = 25 Ω , Vgs = 20 \rightarrow 0 V (see Figure 4.)



(TO-262)



(TO-263)



THERMAL RESISTANCE

Channel to Case Thermal Resistance Rth(ch-C) 1.76 °C/W Channel to Ambient Thermal Resistance Rth(ch-A) 83.3 °C/W

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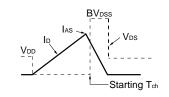


ELECTRICAL CHARACTERISTICS (TA = 25°C)

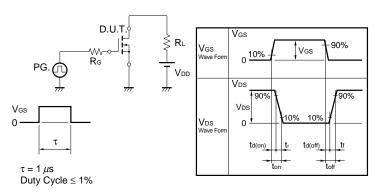
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 55 V, V _{GS} = 0 V			10	μΑ
Gate Leakage Current	Igss	Vgs = ±20 V, Vps = 0 V			±10	μΑ
Gate to Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$	2.0	3.0	4.0	V
Forward Transfer Admittance	yfs	V _{DS} = 10 V, I _D = 24 A	7	17		S
Drain to Source On-state Resistance	RDS(on)	Vgs = 10 V, ID = 24 A		14	17	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V		1600	2400	pF
Output Capacitance	Coss	Vgs = 0 V		250	380	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		120	220	pF
Turn-on Delay Time	t d(on)	V _{DD} = 28 V, I _D = 24 A		22	48	ns
Rise Time	tr	Vgs = 10 V		16	40	ns
Turn-off Delay Time	td(off)	$R_G = 1 \Omega$		35	70	ns
Fall Time	t _f			12	30	ns
Total Gate Charge	Q _G	V _{DD} = 44 V		33	50	nC
Gate to Source Charge	Qgs	Vgs = 10 V		9		nC
Gate to Drain Charge	Q _{GD}	I _D = 48 A		12		nC
Body Diode Forward Voltage	V _{F(S-D)}	IF = 48 A, VGS = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 48 A, VGS = 0 V		40		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		55		nC

TEST CIRCUIT 1 AVALANCHE CAPABILITY

$\begin{array}{c} \text{D.U.T.} \\ \text{Rg} = 25 \, \Omega \\ \text{Vgs} = 20 \rightarrow 0 \, \text{V} \end{array}$



TEST CIRCUIT 2 SWITCHING TIME



TEST CIRCUIT 3 GATE CHARGE

$$\begin{array}{c|c} D.U.T. \\ \hline I_G = 2 \text{ mA} \\ \hline \end{array}$$

$$\begin{array}{c|c} PG. \\ \hline \end{array}$$

$$\begin{array}{c|c} PG. \\ \hline \end{array}$$

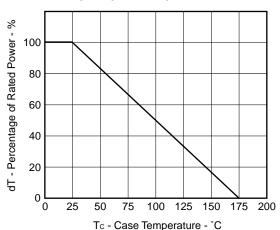
$$\begin{array}{c|c} O.U.T. \\ \hline \end{array}$$

$$\begin{array}{c|c} \hline \end{array}$$

$$\begin{array}{c|c} \hline \\ \hline \end{array}$$

TYPICAL CHARACTERISTICS (TA = 25°C)

Figure 1. DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



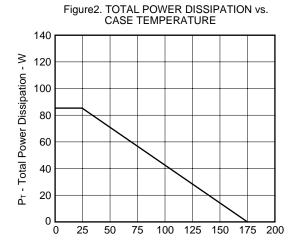


Figure 3. FORWARD BIAS SAFE OPERATING AREA

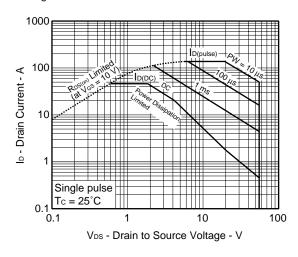


Figure4. SINGLE AVALANCHE ENERGY DERATING FACTOR

Tc - Case Temperature - °C

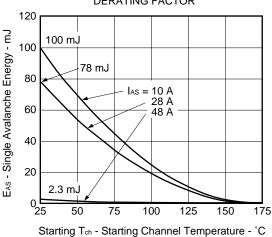
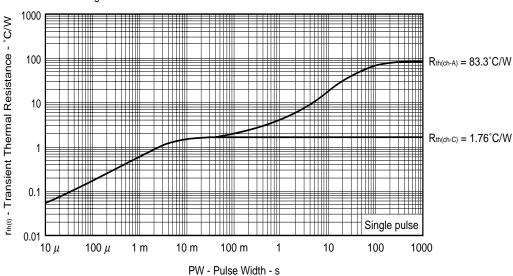
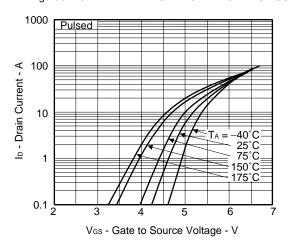


Figure 5. TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



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Figure 6. FORWARD TRANSFER CHARACTERISTICS



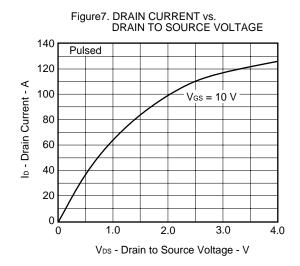
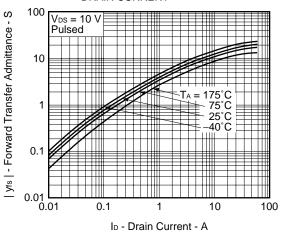


Figure8. FORWARD TRANSFER ADMITTANCE vs. **DRAIN CURRENT**





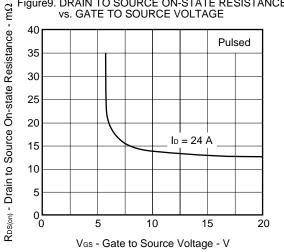


Figure 10. DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

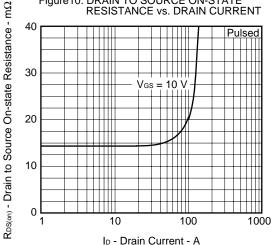
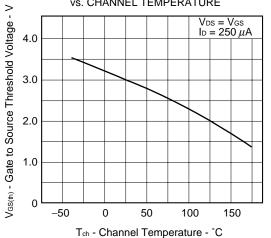
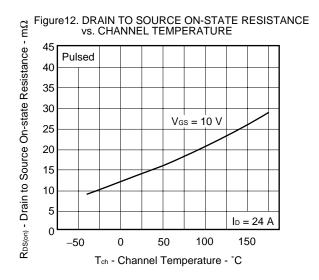
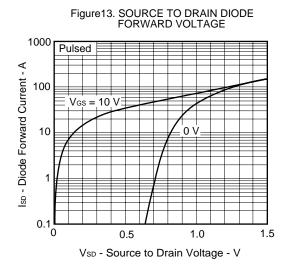
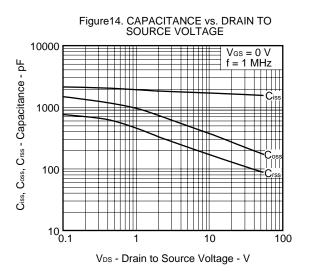


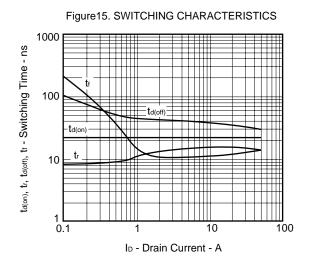
Figure 11. GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE

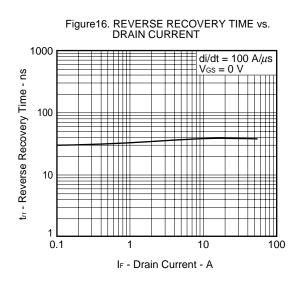


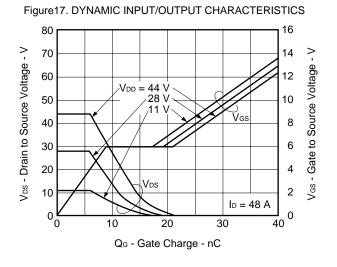








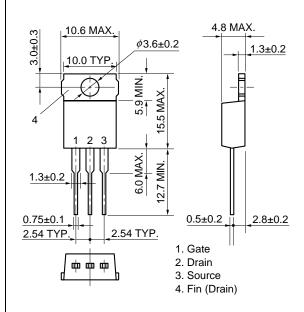




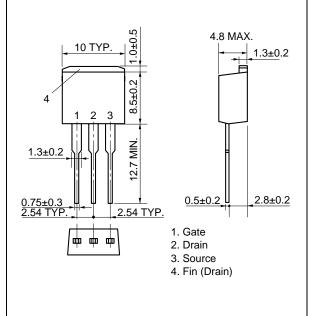
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PACKAGE DRAWINGS (Unit: mm)

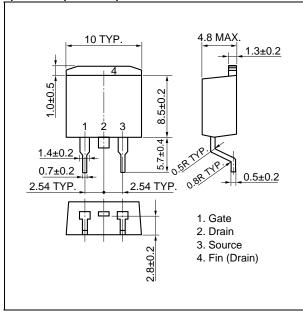




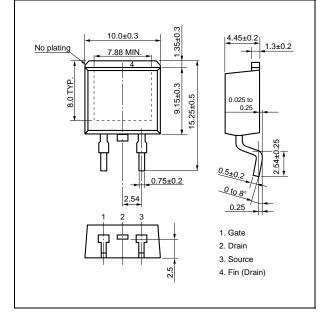
2) TO-262 (MP-25 Fin Cut)



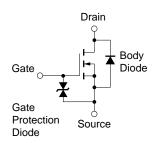
3) TO-263 (MP-25ZJ)



★ 4) TO-263 (MP-25ZK)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.



[MEMO]

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