DATA SHEET



# MOS FIELD EFFECT TRANSISTOR

## NP88N04CHE,NP88N04DHE,NP88N04EHE,NP88N04KHE

**ORDERING INFORMATION** 

PART NUMBER

NP88N04CHE

NP88N04DHE

NP88N04EHE

NP88N04KHE

4

## SWITCHING N-CHANNEL POWER MOS FET

### DESCRIPTION

These products are N-channel MOS Field Effect Transistor designed for high current switching applications.

### FEATURES

- Channel temperature 175 degree rated
- Super low on-state resistance  $R_{\text{DS(on)}} = 4.3 \text{ m}\Omega \text{ MAX. (Vgs} = 10 \text{ V, Id} = 44 \text{ A)}$
- Low Ciss:  $C_{iss} = 7300 \text{ pF TYP}.$
- Built-in gate protection diode

## ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	Vdss	40	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = $25^{\circ}$ C) <sup>Note1</sup>	D(DC)	±88	А
Drain Current (pulse) Note2	D(pulse)	±352	А
Total Power Dissipation ( $T_A = 25^{\circ}C$ )	<b>P</b> T1	1.8	W
Total Power Dissipation (Tc = 25°C)	<b>P</b> T2	288	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	-55 to +175	°C
Single Avalanche Current Note3	las	75/88	А
Single Avalanche Energy Note3	Eas	562/232	mJ

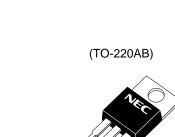
**Notes 1.** Calculated constant current according to MAX. allowable channel temperature.

- **2.** PW  $\leq$  10  $\mu$ s, Duty cycle  $\leq$  1%
- 3. Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 20 V, R<sub>G</sub> = 25  $\Omega$  , V<sub>GS</sub> = 20  $\rightarrow$  0 V (see Figure 4.)

#### THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	0.52	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

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PACKAGE

TO-220AB

TO-262

TO-263 (MP-25ZJ)

TO-263 (MP-25ZK)



(TO-263)



The mark  $\star$  shows major revised points.

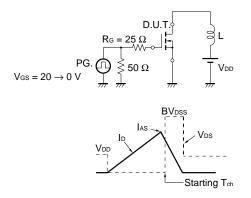
## ELECTRICAL CHARACTERISTICS ( $T_A = 25^{\circ}C$ )

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 40 V, V_{GS} = 0 V$			10	μA
Gate Leakage Current	lgss	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±10	μA
Gate to Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.0	3.0	4.0	V
Forward Transfer Admittance	yfs	Vds = 10 V, Id = 44 A	30	60		s
Drain to Source On-state Resistance	RDS(on)	Vgs = 10 V, Id = 44 A		3.4	4.3	mΩ
Input Capacitance	Ciss	Vds = 25 V		7300	11000	pF
Output Capacitance	Coss	Vgs = 0 V		1400	2100	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		620	1120	pF
Turn-on Delay Time	td(on)	$V_{DD} = 20 V, I_D = 44 A$		38	84	ns
Rise Time	tr	Vgs = 10 V		27	68	ns
Turn-off Delay Time	td(off)	Rg = 1 Ω		110	220	ns
Fall Time	tr			32	80	ns
Total Gate Charge	Q <sub>G</sub>	Vdd = 32 V		120	180	nC
Gate to Source Charge	QGS	Vgs = 10 V		30		nC
Gate to Drain Charge	Qgd	ID = 88 A		43		nC
Body Diode Forward Voltage	VF(S-D)	IF = 88 A, VGS = 0 V		0.95		V
Reverse Recovery Time	trr	IF = 88 A, VGS = 0 V		64		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/µs		99		nC

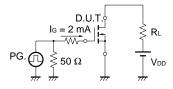
#### TEST CIRCUIT 1 AVALANCHE CAPABILITY

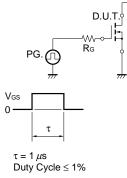
#### **TEST CIRCUIT 2 SWITCHING TIME**

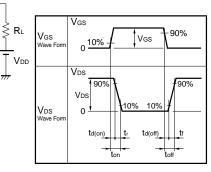
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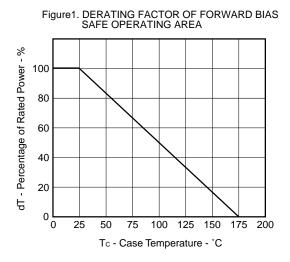
#### **TEST CIRCUIT 3 GATE CHARGE**







#### TYPICAL CHARACTERISTICS $(T_A = 25^{\circ}C)$



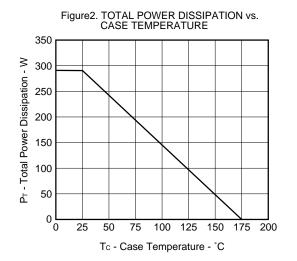
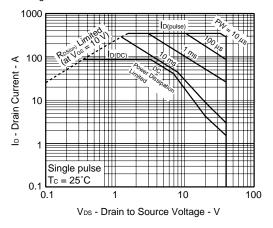


Figure3. FORWARD BIAS SAFE OPERATING AREA



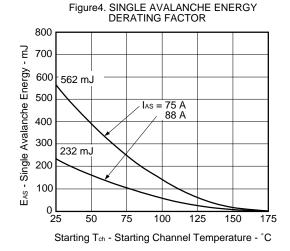


Figure5. TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

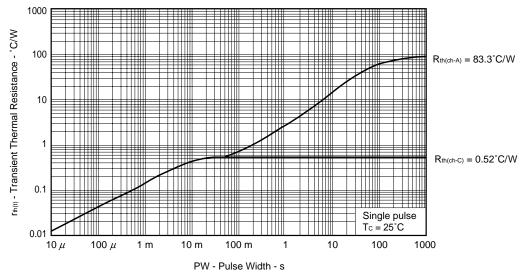
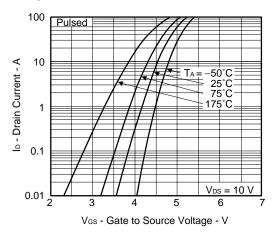


Figure6. FORWARD TRANSFER CHARACTERISTICS



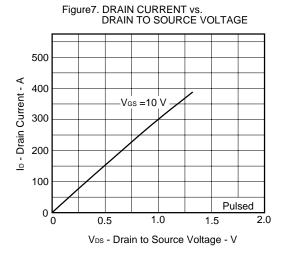
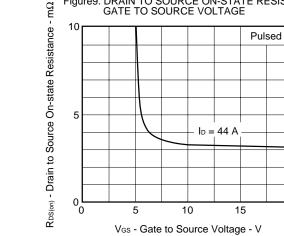


Figure9. DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



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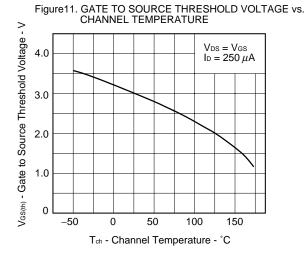
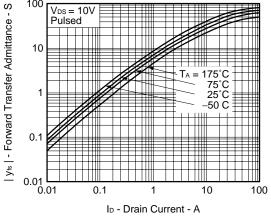
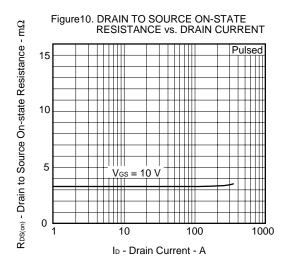


Figure8. FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT 100 Vos = 10V





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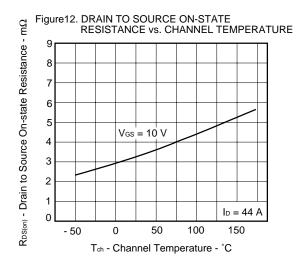


Figure 13. SOURCE TO DRAIN DIODE FORWARD VOLTAGE

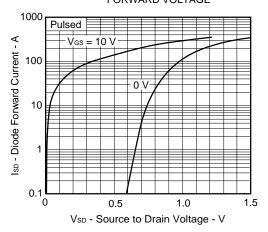


Figure 15. SWITCHING CHARACTERISTICS

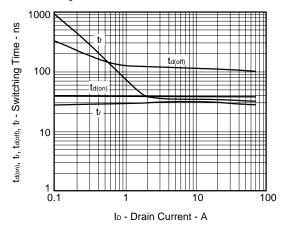


Figure17. DYNAMIC INPUT/OUTPUT CHARACTERISTICS

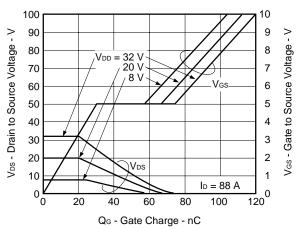
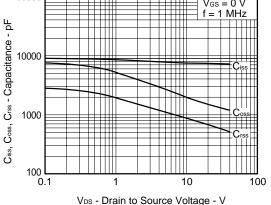
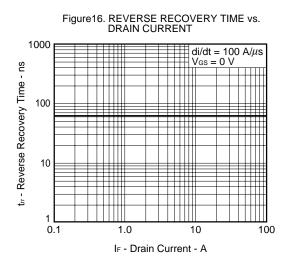


Figure 14. CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

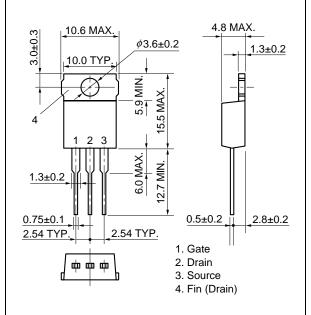


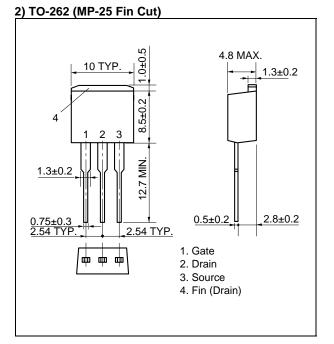




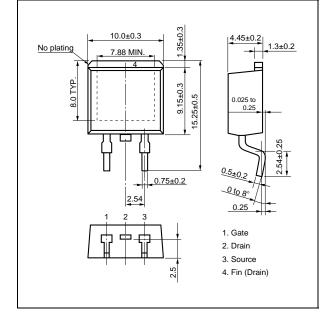
## PACKAGE DRAWINGS (Unit: mm)

#### 1) TO-220AB (MP-25)



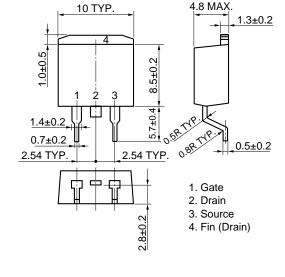


#### ★ 4) TO-263 (MP-25ZK)

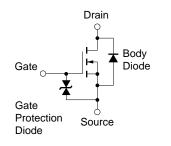


## 10 TYP

3) TO-263 (MP-25ZJ)



## EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

[MEMO]

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