

MOS FIELD EFFECT TRANSISTOR

NP88N055CHE,NP88N055DHE,NP88N055EHE,NP88N055KHE

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

These products are N-channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Channel temperature 175 degree rated
- Super low on-state resistance $R_{DS(on)} = 5.3 \text{ m}\Omega$ MAX. (Vgs = 10 V, ID = 44 A)
- Low Ciss: Ciss = 7600 pF TYP.
- Built-in gate protection diode

ORDERING INFORMATION

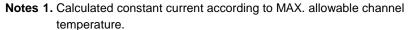
PART NUMBER	PACKAGE
NP88N055CHE	TO-220AB
NP88N055DHE	TO-262
NP88N055EHE	TO-263 (MP-25ZJ)
NP88N055KHE	TO-263 (MP-25ZK)

(TO-220AB)

(TO-262)

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	55	V
Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C) Note1	ID(DC)	±88	Α
Drain Current (Pulse) Note2	ID(pulse)	±352	Α
Total Power Dissipation (T _A = 25°C)	P _{T1}	1.8	W
Total Power Dissipation (Tc = 25°C)	P _{T2}	288	W
Channel Temperature	Tch	175	°C
Storage Temperature	T _{stg}	-55 to +175	°C
Single Avalanche Current Note3	las	65 / 88	Α
Single Avalanche Energy Note3	Eas	422 / 15	mJ



- **2.** PW \leq 10 μ s, Duty Cycle \leq 1%
- 3. Starting T_{ch} = 25°C, V_{DD} = 28 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V (See Figure 4.)

(TO-263)

THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	0.52	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W



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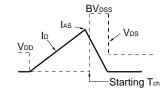
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ELECTRICAL CHARACTERISTICS (TA = 25°C)

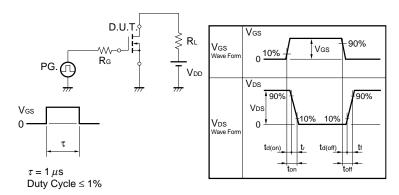
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CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 55 V, V _{GS} = 0 V			10	μΑ
Gate Leakage Current	Igss	Vgs = ±20 V, Vps = 0 V			±10	μΑ
Gate to Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$	2.0	3.0	4.0	V
Forward Transfer Admittance	y _{fs}	V _{DS} = 10 V, I _D = 44 A	30	60		S
Drain to Source On-state Resistance	RDS(on)	Ves = 10 V, ID = 44 A		4.2	5.3	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V		7600	11400	pF
Output Capacitance	Coss	Ves = 0 V		1100	1700	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		480	870	pF
Turn-on Delay Time	t d(on)	V _{DD} = 28 V, I _D = 44 A		42	93	ns
Rise Time	tr	Ves = 10 V		26	66	ns
Turn-off Delay Time	td(off)	$R_G = 1 \Omega$		120	240	ns
Fall Time	t _f			32	81	ns
Total Gate Charge	Q _G	V _{DD} = 44 V		130	200	nC
Gate to Source Charge	Qgs	Ves = 10 V		31		nC
Gate to Drain Charge	Q _{GD}	Ib = 88 A	_	49		nC
Body Diode Forward Voltage	V _{F(S-D)}	IF = 88 A, Vgs = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 88 A, Vgs = 0 V		62		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		120		nC

TEST CIRCUIT 1 AVALANCHE CAPABILITY

$\begin{array}{c} \text{D.U.T.} \\ \text{RG} = 25 \ \Omega \\ \text{VGS} = 20 \rightarrow 0 \ \text{V} \end{array} \begin{array}{c} \text{D.U.T.} \\ \text{S} \\ \text{S} \\ \text{S} \\ \text{O} \end{array} \begin{array}{c} \text{D.U.T.} \\ \text{I.I.} \\ \text{VDD} \\ \text{M.M.} \end{array}$



TEST CIRCUIT 2 SWITCHING TIME



TEST CIRCUIT 3 GATE CHARGE

TYPICAL CHARACTERISTICS (TA = 25°C)

Figure 1. DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

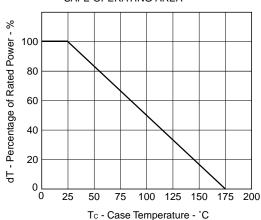


Figure2. TOTAL POWER DISSIPATION vs. CASE TEMPERATURE

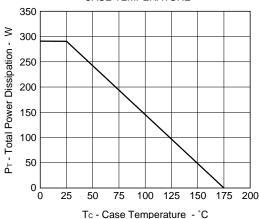


Figure3. FORWARD BIAS SAFE OPERATING AREA

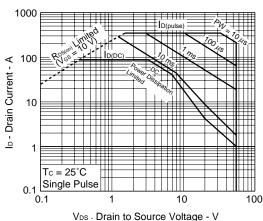
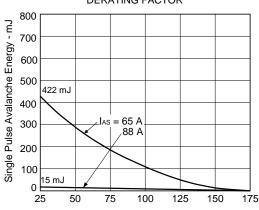


Figure4. SINGLE AVALANCHE ENERGY DERATING FACTOR



Starting Tch - Starting Channel Temperature - °C

Figure 5. TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

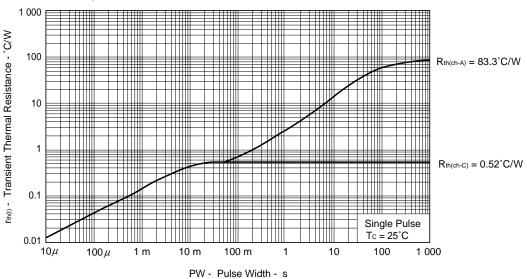


Figure 6. FORWARD TRANSFER CHARACTERISTICS

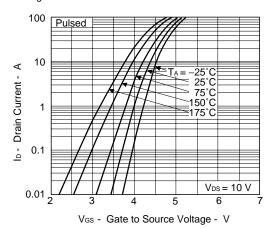


Figure 8. FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

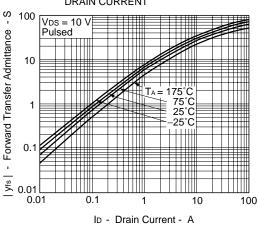


Figure 10. DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

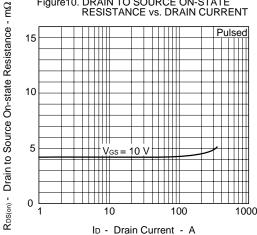
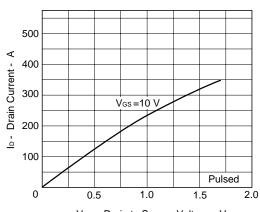


Figure 7. DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



VDS - Drain to Source Voltage - V

Figure9. DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

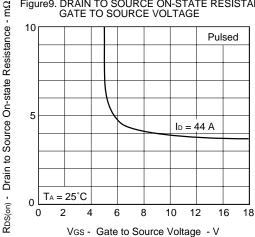
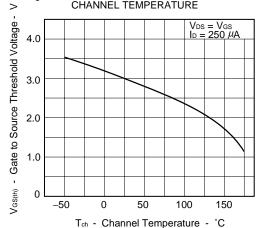
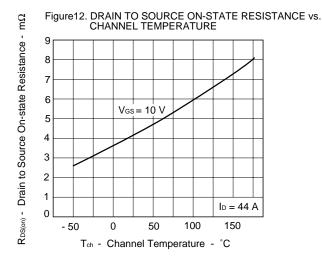
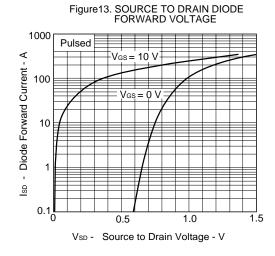
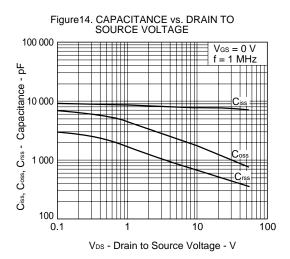


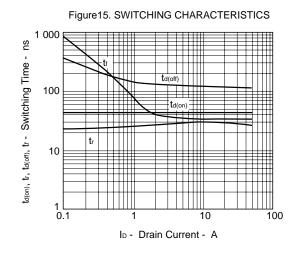
Figure11. GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE

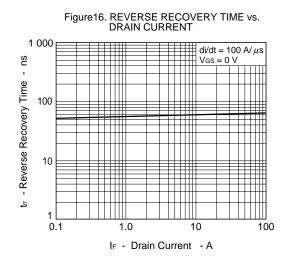


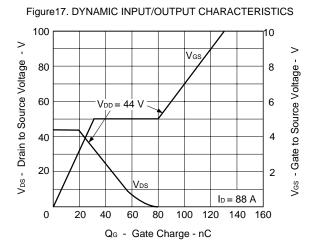








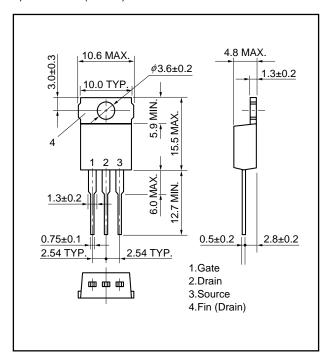




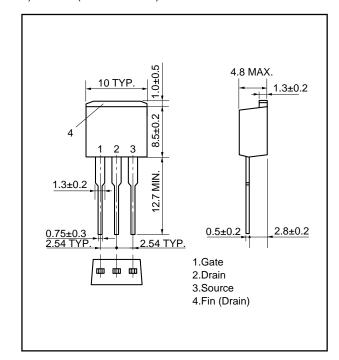
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PACKAGE DRAWINGS (Unit: mm)

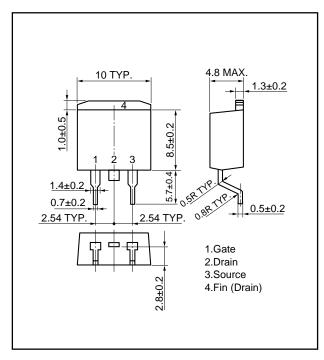
1) TO-220AB (MP-25)



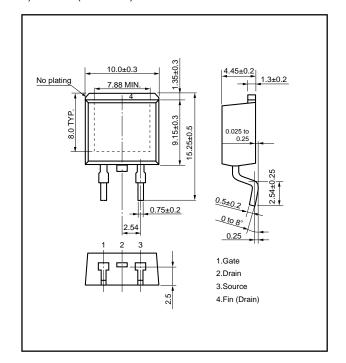
2) TO-262 (MP-25 Fin Cut)



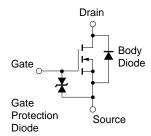
3) TO-263 (MP-25ZJ)



★ 4) TO-263 (MP-25ZK)



EQUIVALENT CIRCUIT



Remark

The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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