New Jersey Semi-Conductor Products, Inc.

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RFP8N20L

8A, 200V, 0.600 Ohm, Logic Level, N-Channel Power MOSFET

This N-Channel enhancement mode silicon gate power field effect transistor is specifically designed for use with logic level (5V) driving sources in applications such as programmable controllers, automotive switching and solenoid drivers. This performance is accomplished through a special gate oxide design which provides full rated conduction at gate biases in the 3V to 5V range, thereby facilitating true on-off power control directly from logic circuit supply voltages.

Ordering Information

PART NUMBER	PACKAGE	BRAND
RFP8N20L	TO-220AB	RFP8N20L

NOTE: When ordering, include the entire part number.

Features

- 8A, 200V
- r_{DS(ON)} = 0.600Ω
- · Design Optimized for 5V Gate Drives
- Can be Driven Directly from QMOS, NMOS, TTL Circuits
- Compatible with Automotive Drive Requirements
- SOA is Power Dissipation Limited
- Nanosecond Switching Speeds
- · Linear Transfer Characteristics
- · High Input Impedence
- Majority Carrier Device

Symbol



Packaging

JEDEC TO-220AB





NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

Quality Semi-Conductors

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RFP8N20L

Absolute Maximum Ratings T_{C} = 25°C, Unless Otherwise Specified

	RFP8N20L	UNITS
Drain to Source Voltage (Note 1)V _{DS}	200	V
Drain to Gate Voltage (R _{GS} = 20kΩ) (Note 1)	200	V
Continuous Drain Current	8	А
Pulsed Drain Current (Note 3)	20	А
Gate to Source Voltage	10	V
Maximum Power Dissipation	60	W
Above T _C = 25 ^o C, Derate Linearly	0.48	W/ ^o C
Operating and Storage Temperature	-55 to 150	°C
Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10sT _L Package Body for 10s, See Techbrief 334	300 260	°C °C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

NOTE:

1. T_J = 25°C to 125°C.

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNITS
Drain to Source Breakdown Voltage	BV _{DSS}	$I_{\rm D} = 250 \mu A, V_{\rm GS} = 0$		200	-	-	V
Gate to Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$		1	-	2	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 0.8 x Rated BV _{DSS}	T _C = 25°C	-	-	1	μA
		V _{DS} = 0.8 x Rated BV _{DSS}	T _C = 125°C	-	-	50	μA
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = 10V, V_{DS} = 0$		-	-	100	nA
Drain to Source On-Voltage (Note 2)	V _{DS(ON)}	I _D = 8A, V _{GS} = 5V		-	-	4.8	V
Drain to Source On Resistance (Note 2)	^r DS(ON)	I _D = 4A, V _{GS} = 5V		-	-	0.600	Ω
Turn On Delay Time	t _{d(ON)}	I _D = 4A, V _{DD} = 50V, R _G = 6.25 , V _{GS} = 5V		-	15	45	ns
Rise Time	tr			-	45	150	ns
Turn Off Delay Time	t _{d(OFF)}			-	100	135	ns
Fall Time	t _f			-	60	105	ns
Input Capacitance	CISS	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		-	-	900	pF
Output Capacitance	C _{OSS}				-	250	pF
Reverse Transfer Capacitance	C _{RSS}			-	-	120	pF
Thermal Resistance Junction to Case	R _{θJC}	RFP8N20L				2.083	°C/W

Electrical Specifications T_C = 25°C, Unless Otherwise Specified

Source to Drain Diode Specifications

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	ТҮР	MAX	UNITS
Source to Drain Diode Voltage (Note 2)	V _{SD}	I _{SD} = 4A	-	-	1.4	V
Diode Reverse Recovery Time	t _{rr}	I _{SD} = 4A, dI _{SD} /dt = 100A/μs	-	250	-	ns

NOTES:

2. Pulsed: pulse duration = 300µs max, duty cycle = 2%.

3. Repetitive rating: pulse width limited by maximum junction temperature.