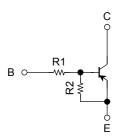
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

## **RN2975**

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications.

- Two devices are incorporated into an Ultra-Super-Mini (6 pin) package
- Incorporating a bias resistor into a transistor reduces parts count.
   Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.

### **Equivalent Circuit and Bias Resistor Values**



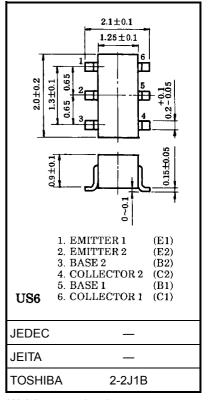
R1:  $2.2 \text{ k}\Omega$  (Q1, Q2 common) R2:  $10 \text{ k}\Omega$  (Q1, Q2 common)

### Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-50	٧
Collector-emitter voltage	$V_{CEO}$	-50	٧
Emitter-base voltage	V <sub>EBO</sub>	-6	V
Collector current	Ic	-100	mA
Collector power dissipation	P <sub>C</sub> (Note)	200	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	<b>−55~150</b>	°C

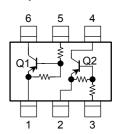
Note: Total rating

Unit: mm



Weight: g (typ.)

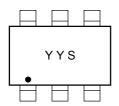
# Equivalent Circuit (top view)



## Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	- nA
	I <sub>CEO</sub>	$V_{CE} = -50 \text{ V}, I_B = 0$	_		-500	
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = -6 \text{ V}, I_{C} = 0$	-0.37	_	-0.71	mA
DC current gain	h <sub>FE</sub>	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	50	_	_	
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	V <sub>I (ON)</sub>	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-0.6	_	-2.5	V
Input voltage (OFF)	V <sub>I (OFF)</sub>	$V_{CE} = -5 \text{ V}, I_{C} = -0.1 \text{ mA}$	-0.3	_	-1.0	V
Input resistor	R1	_	1.54	2.2	2.86	kΩ
Resistor ratio	R1/R2	_	_	0.22	_	

## Marking



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