TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSII)

# **TPC6001**

# Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance:  $RDS(ON) = 22 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance:  $|Y_{fs}| = 15 \mathrm{S}$  (typ.)
- Low leakage current:  $IDSS = 10 \mu A (max) (VDS = 20 V)$
- Enhancement-model:  $V_{th}$  = 0.5 to 1.2 V ( $V_{DS}$  = 10 V,  $I_{D}$  = 200  $\mu A$ )

#### **Maximum Ratings (Ta = 25°C)**

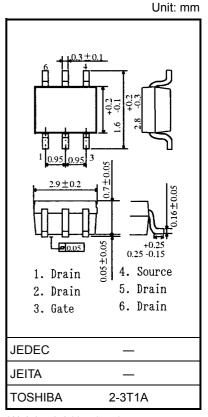
Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	20	٧	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	20	V	
Gate-source voltage		$V_{GSS}$	±12	V	
Drain current	DC (Note 1)	Ι <sub>D</sub>	6	Α	
	Pulse (Note 1)	I <sub>DP</sub>	24		
Drain power dissipation	(t = 5 s) (Note 2a)	P <sub>D</sub>	2.2	W	
Drain power dissipation	(t = 5 s) (Note 2b)	P <sub>D</sub>	0.7	W	
Single pulse avalanche ene	E <sub>AS</sub>	5.8	mJ		
Avalanche current	I <sub>AR</sub>	3	Α		
Repetitive avalanche energy (Note 4)		E <sub>AR</sub>	0.22	mJ	
Channel temperature	T <sub>ch</sub>	150	°C		
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t=5\;\text{s}) \tag{Note 2a}$	R <sub>th (ch-a)</sub>	56.8	°C/W
Thermal resistance, channel to ambient $(t=5\;s) \eqno(Note\;2b)$	R <sub>th (ch-a)</sub>	178.5	°C/W

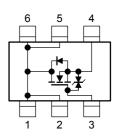
Note: (Note 1), (Note 2), (Note 3), (Note 4), (Note 5) Please see next page.

This transistor is an electrostatically sensitive device. Please handle it with caution.

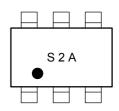


Weight: 0.011 g (typ.)

#### **Circuit Configuration**



#### Marking (Note 5)



# **Electrical Characteristics (Ta = 25°C)**

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Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-OFF cu	ırrent	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	20	_	_	V
		V <sub>(BR) DSX</sub>	$I_D = 10 \text{ mA}, V_{GS} = -12 \text{ V}$	8	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_D = 200 \mu\text{A}$	0.5	_	1.2	V
		R <sub>DS</sub> (ON)	$V_{GS} = 2.0 \text{ V}, I_D = 3 \text{ A}$	_	35	60	
Drain-source ON	resistance	R <sub>DS</sub> (ON)	$V_{GS} = 2.5 \text{ V}, I_D = 3 \text{ A}$	_	28	45	mΩ
		R <sub>DS</sub> (ON)	$V_{GS} = 4.5 \text{ V}, I_D = 3 \text{ A}$	_	22	30	
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3 A	7.5	15	_	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	755	_	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	172	_	
Output capacitance		Coss		_	222	_	
Switching time  Turn-ON time  Fall time  Turn-OFF time	Rise time	t <sub>r</sub>	ACS 0 N	_	6	_	ns
	Turn-ON time	t <sub>on</sub>		_	11	_	
	Fall time	t <sub>f</sub>		_	32	_	
	Turn-OFF time	t <sub>off</sub>	$V_{DD} \simeq 10 \text{ V}$ Duty $\leq 1\%$ , $t_W = 10 \mu\text{s}$	_	64	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 16 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 6 \text{ A}$	_	15	_	nC
Gate-source charge		Q <sub>gs</sub>		_	10	_	
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	5	_	

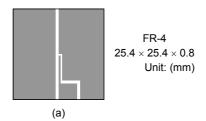
# **Source-Drain Ratings and Characteristics (Ta = 25°C)**

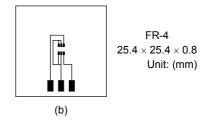
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Pulse drain reverse current	(Note 1)	I <sub>DRP</sub>	_	_	_	24	Α
Forward voltage (diode)		$V_{DSF}$	$I_{DR} = 6 A$ , $V_{GS} = 0 V$	_	_	-1.2	V

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

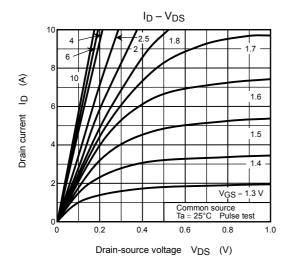


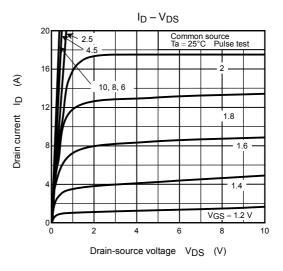


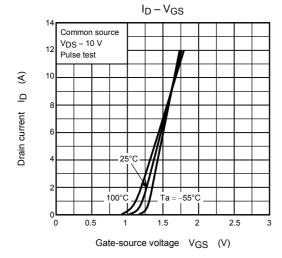
Note 3:  $V_{DD} = 16 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 0.5 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 3.0 \text{ A}$ 

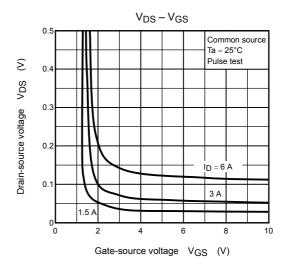
Note 4: Repetitive rating; pulse width limited by maximum channel temperature

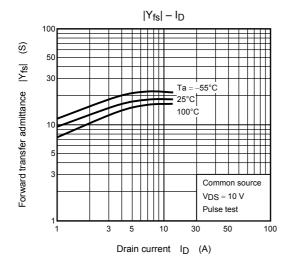
Note 5: Black round marking "•" locates on the left lower side of parts number marking "S2A" indicates terminal No.1.

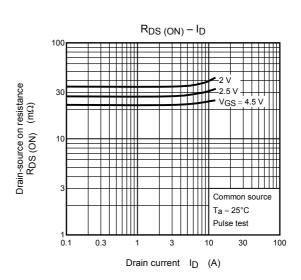




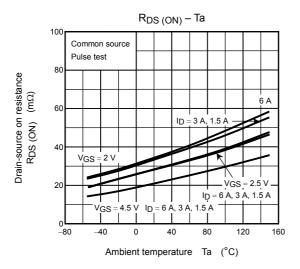


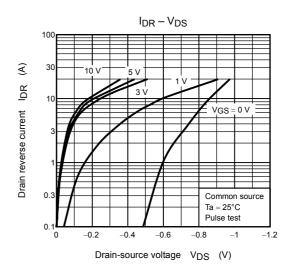


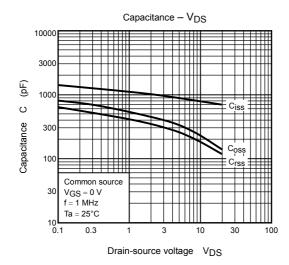


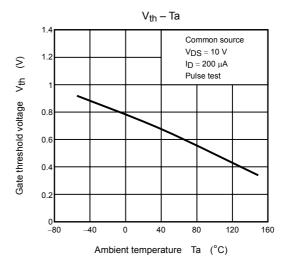


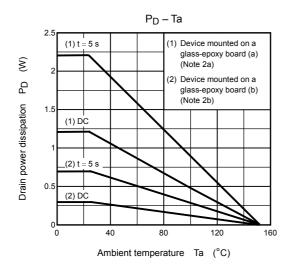
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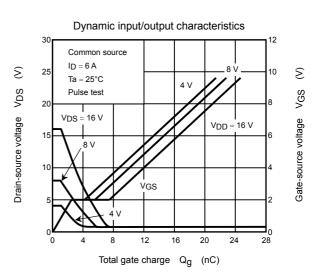


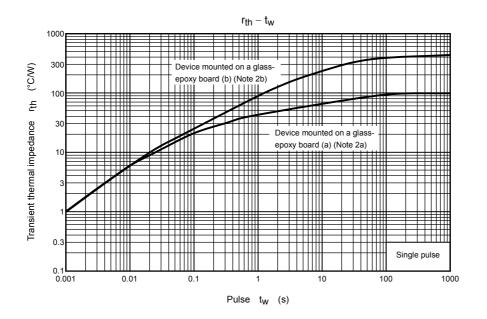


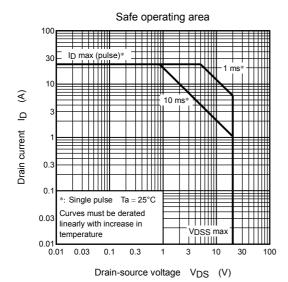












5

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