TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIII)

TPC6004

Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance: RDS (ON) = 19 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 11 \text{ S (typ.)}$
- Low leakage current: $IDSS = 10 \mu A (max) (VDS = 20 V)$
- Enhancement-model: V_{th} = 0.5 to 1.2 V (V_{DS} = 10 V, I_{D} = 200 μA)

Maximum Ratings (Ta = 25°C)

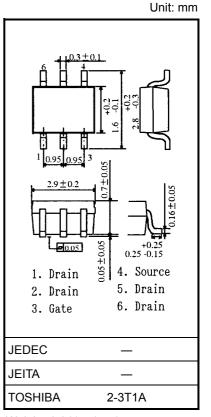
Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	20	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	20	V	
Gate-source voltage		V_{GSS}	±12	٧	
	DC	ls.	6	Α	
Drain current	(Note 1)	Ι _D	0		
Drain current	Pulse	<u> </u>	24		
	(Note 1)	I _{DP}	24	ı	
Drain power dissipation	(t = 5 s)	PD	2.2	W	
	(Note 2a)	۲۵	2.2	VV	
Drain power dissipation	(t = 5 s)	PD	0.7	W	
	(Note 2b)	۲۵	0.7	VV	
Single pulse avalanche ene	E _{AS}	5.8	mJ		
Avalanche current	I _{AR}	3	Α		
Repetitive avalanche energy (Note 4)		E _{AR}	0.22	mJ	
Channel temperature	T _{ch}	150	°C		
Storage temperature range		T _{stg}	-55 to 150	°C	

Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient $(t = 5 \text{ s})$ (Note 2a)	R _{th (ch-a)}	56.8	°C/W	
Thermal resistance, channel to ambient $(t=5\;s) \eqno(Note\;2b)$	R _{th (ch-a)}	178.5	°C/W	

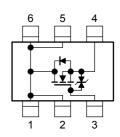
Note: (Note 1), (Note 2), (Note 3), (Note 4), (Note 5) Please see next page.

This transistor is an electrostatically sensitive device. Please handle it with caution.

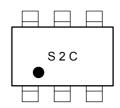


Weight: 0.011 g (typ.)

Circuit Configuration



Marking (Note 5)





Electrical Characteristics (Ta = 25°C)

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-OFF cu	rrent	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V		_	10	μΑ
Drain-source breakdown voltage		V _{(BR) DSS}	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	20	_		V
		V _{(BR) DSX}	$I_D = 10 \text{ mA}, V_{GS} = -12 \text{ V}$	8	_		
Gate threshold vo	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_D = 200 \mu\text{A}$	0.5	_	1.2	V
			$V_{GS} = 2.0 \text{ V}, I_D = 3 \text{ A}$	_	30	37	mΩ
Drain-source ON resistance	R _{DS (ON)}	$V_{GS} = 2.5 \text{ V}, I_D = 3 \text{ A}$	_	25	32		
			$V_{GS} = 4.5 \text{ V}, I_D = 3 \text{ A}$	_	19	24	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3 A	5.5	11	_	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	1400	_	pF
Reverse transfer capacitance		C _{rss}		_	165	_	
Output capacitance		Coss		_	180	_	
Switching time	Rise time	t _r	$V_{GS} \stackrel{5}{\underset{0}{\bigvee}} V \stackrel{I_{D}}{\underset{M}{\bigvee}} = 3 \text{ A}$ $V_{OUT} \stackrel{C}{\underset{M}{\bigvee}} V_{OUT}$ $V_{DD} \approx 10 \text{ V}$ $V_{DU} \approx 10 \text{ µs}$	_	5	_	
	Turn-ON time	t _{on}		_	10	_	ns
	Fall time	t _f		_	14	_	
	Turn-OFF time	t _{off}		_	60	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 16 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 6 \text{ A}$	_	17	_	nC
Gate-source charge		Q _{gs}			13	_	
Gate-drain ("miller") charge		Q _{gd}			4	_	

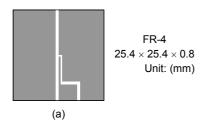
Source-Drain Ratings and Characteristics (Ta = 25°C)

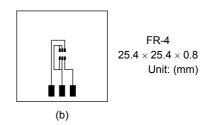
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Pulse drain reverse current	(Note 1)	I _{DRP}	_	_	_	24	Α
Forward voltage (Diode)		V_{DSF}	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

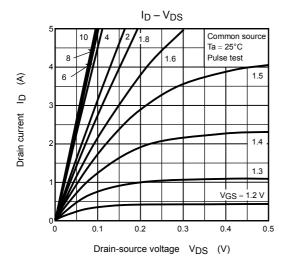


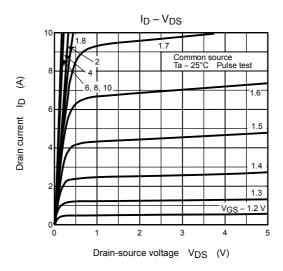


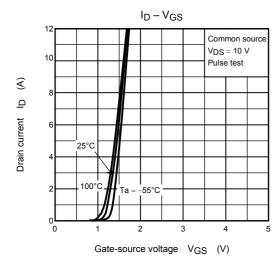
Note 3: V_{DD} = 16 V, T_{ch} = 25°C (initial), L = 0.5 mH, R_G = 25 Ω , I_{AR} = 3.0 A

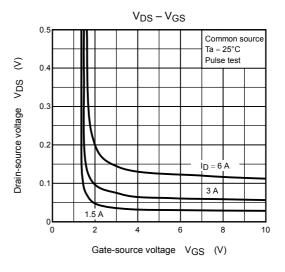
Note 4: Repetitive rating; pulse width limited by maximum channel temperature

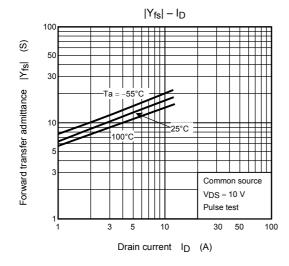
Note 5: Black round marking "●" locates on the left lower side of parts number marking "S2C" indicates terminal No.1.

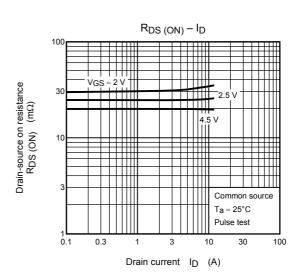


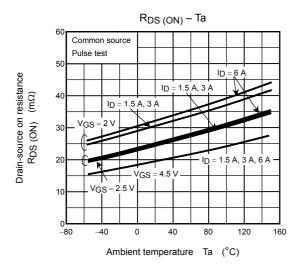


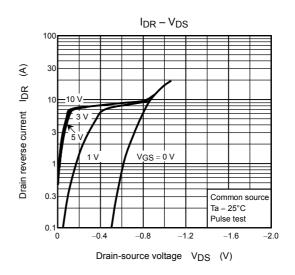


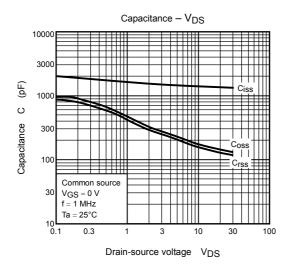


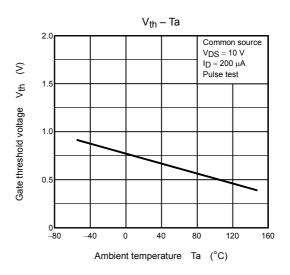


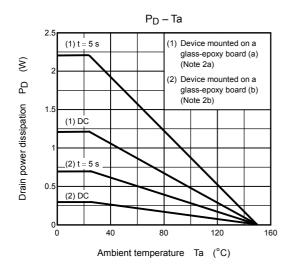


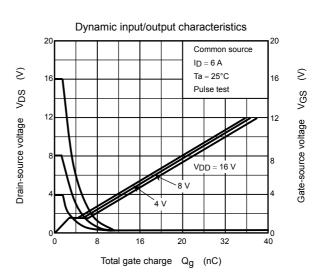


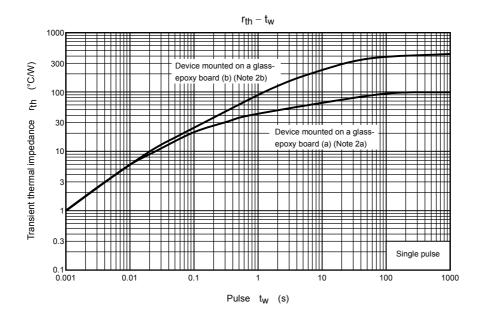


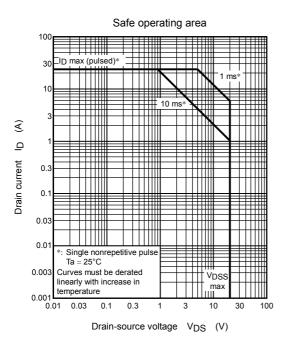












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