TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (High Speed U-MOSII)

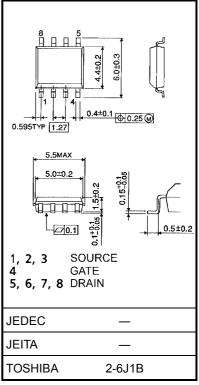
TPC8006-H

High Speed and High Efficiency DC–DC Converters Portable Equipment Applications Notebook PC Applications

- Small footprint due to small and thin package
- High speed switching
- Small gate charge : Qg = 16 nC (typ.)
- Low drain-source ON resistance $: R_{DS} (ON) = 19 \text{ m}\Omega (typ.)$
- High forward transfer admittance : $|Y_{fs}| = 8.8 \text{ S}$ (typ.)
- Low leakage current $: I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 30 \ V)$
- Enhancement-mode : $V_{th} = 1.3 \sim 2.5 \text{ V} (V_{DS} = 10 \text{ V}, \text{ ID} = 1 \text{ mA})$

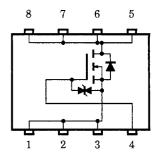
Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	30	V	
Drain-gate voltage (R	t _{GS} = 20 kΩ)	V _{DGR}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	7	A	
	Pulse (Note 1)	I _{DP}	28		
Drain power dissipati	on (t = 10 s) (Note 2a)	PD	2.4	W	
Drain power dissipation (t = 10 s) (Note 2b)		PD	1.0	W	
Single pulse avalancl	ne energy (Note 3)	E _{AS}	63.7	mJ	
Avalanche current		I _{AR}	7	А	
Repetitive avalanche	energy Note 2a) (Note 4)	E _{AR}	0.24	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	−55 to 150	°C	

Maximum Ratings (Ta = 25°C)



Weight: 0.080 g (typ.)

Circuit Configuration



Note: For (Note 1), (Note 2), (Note 3) and (Note 4), please refer to the next page.

This transistor is an electrostatic sensitive device. Please handle with caution.

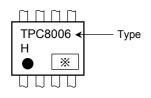
Unit: mm

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Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	52.1	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

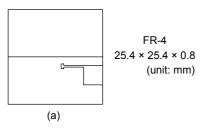
Marking (Note 5)

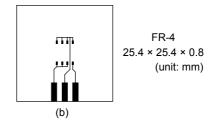


Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)







Note 3: V_{DD} = 24 V, T_{ch} = 25°C (initial), L = 1.0 mH, R_G = 25 Ω , I_{AR} = 7 A

Note 4: Reptitve rating; pulse width limited by maximum channel temperature

Note 5: ● on lower left of the marking indicates Pin 1.

% shows lot number. (year of manufacture: last decimal digit of the year of manufacture, month of manufacture: January to December are denoted by letters A to L respectively.)

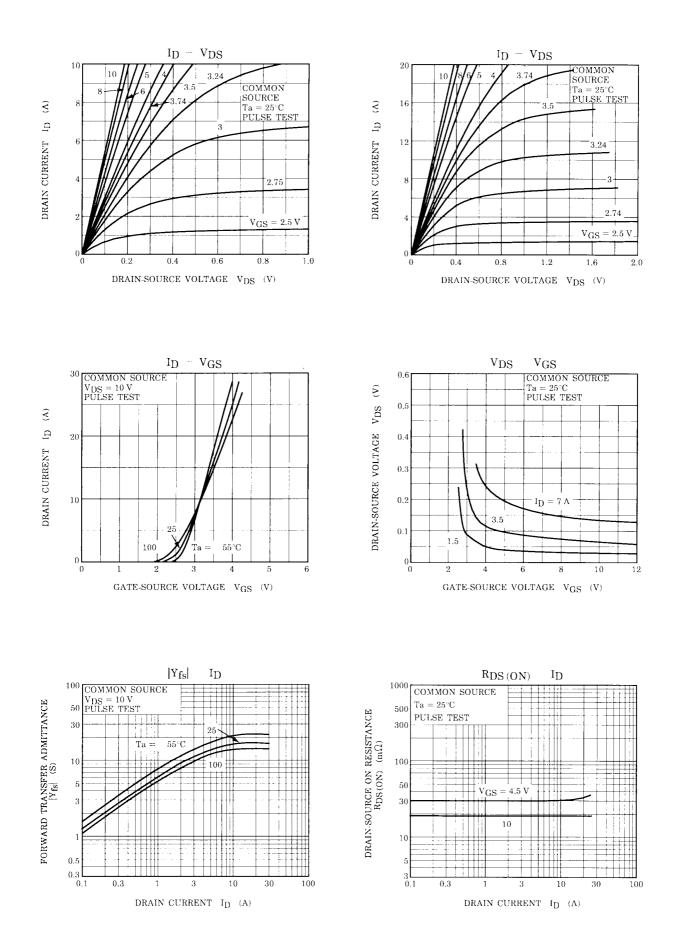
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			10	μA
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	30	_	—	V
		V (BR) DSX	I _D = 10 mA, V _{GS} = -20 V		-	—	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.3	-	2.5	V
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = 4.5 V, I _D = 3.5 A	_	29	40	mΩ
		R _{DS (ON)}	V _{GS} = 10 V, I _D = 3.5 A		19	27	mΩ
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3.5 A	4.4	8.8	—	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	790	—	pF
Reverse transfer capacitance		C _{rss}		_	110	_	
Output capacitance		C _{oss}		_	290	—	
Switching time	Rise time	tr	$V_{GS} \stackrel{10 \text{ V}}{_{0 \text{ V}}} \prod_{\substack{O \text{ V} \\ O \text{ V} \\ \downarrow}} I_{D} = 3.5 \text{ A}$ V_{OUT} $R_{L} = 4.3 \Omega$ $V_{DD} = 15 \text{ V}$ $Duty \leq 1\%, t_{W} = 10 \mu \text{s}$		5	_	
	Turn-on time	t _{on}		_	13	_	
	Fall time	t _f		_	8	_	ns
	Turn-off time	t _{off}		_	36	_	
Total gate charge (Gate-source plus gate-drain)		Qg			16	_	nC
Gate-source charge		Q _{gs}	V _{DD} ≈ 24 V, V _{GS} = 10 V, I _D = 7 A	—	12	—	
Gate-drain ("miller") charge		Q _{gd}			4	—	

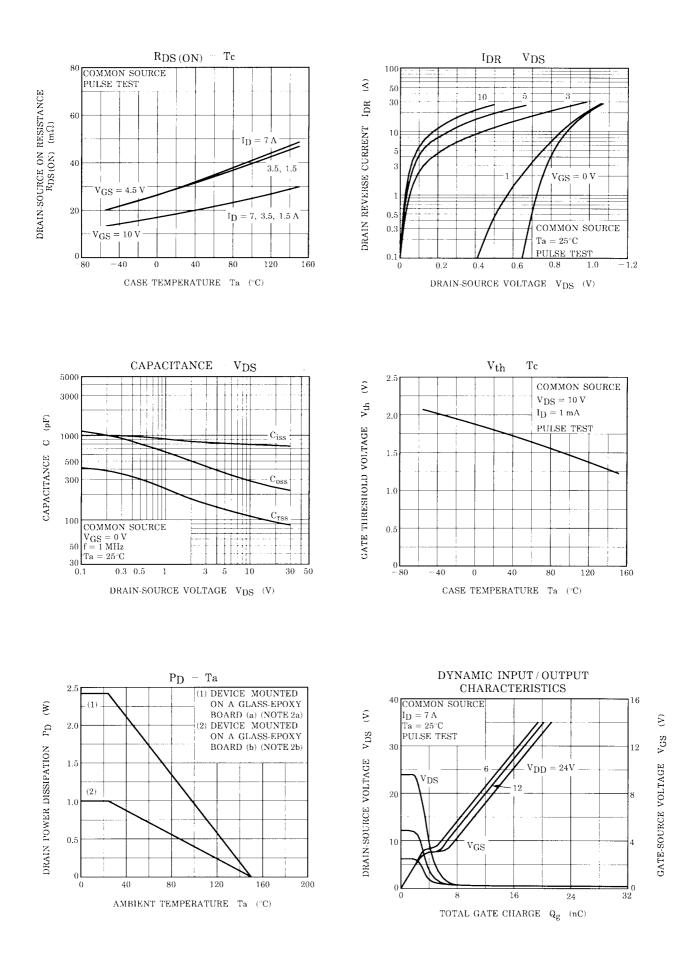
Source–Drain Ratings and Characteristics (Ta = 25°C)

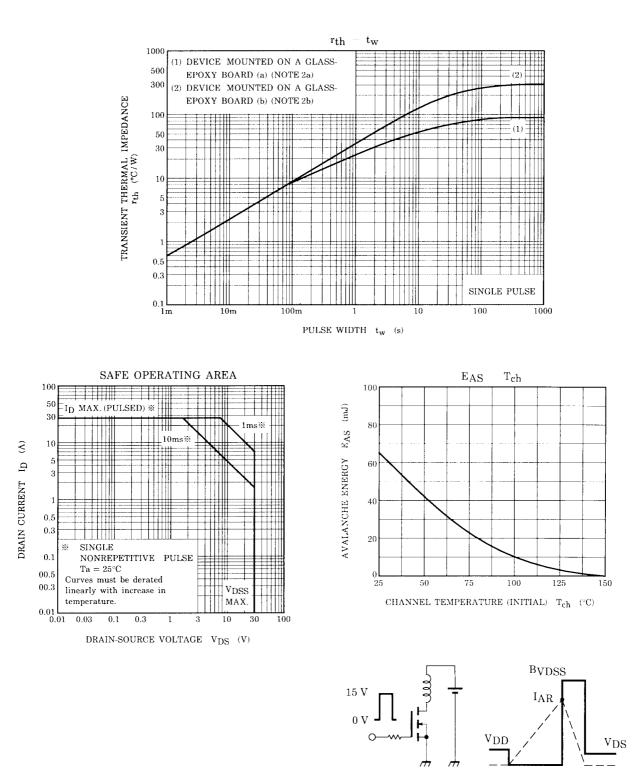
Charact	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	—	_	_	28	А
Forward voltage (diode) V _{DSF}		V _{DSF}	I _{DR} = 7 A, V _{GS} = 0 V			-1.2	V

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TEST CIRCUIT WAVE FORM

 $\begin{array}{l} T_{ch}=25\,^{\circ}C \ (Initial) \\ Peak \ I_{AR}=7 \ A, \ R_{G}=25 \ \Omega \\ V_{DD}=24 \ V, \ L=1.0 \ mH \end{array} \\ \end{array} \\ \begin{array}{l} E_{AS}=\frac{1}{2} \cdot L \ \cdot I^{2} \cdot \left(\frac{B_{VDSS}}{B_{VDSS}-V_{DD}} \right) \end{array}$

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