TPC8107

TOSHIBA

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIII)

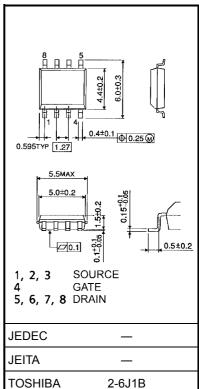
TPC8107

Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

- Small footprint due to small and thin package
- Low drain-source ON resistance: R_{DS} (ON) = 5.5 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 31 \text{ S} (typ.)$
- Low leakage current: $I_{DSS} = -10 \ \mu A \ (max) \ (V_{DS} = -30 \ V)$
- Enhancement-mode: $V_{th} = -0.8$ to -2.0 V ($V_{DS} = -10$ V, $I_D = -1$ mA)

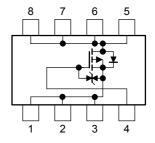
Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	-30	V	
Drain-gate voltage (R	k _{GS} = 20 kΩ)	V _{DGR}	-30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	-13	А	
Drain current	· · · · PD	-52	~		
Drain power dissipati	on (t = 10 s) (Note 2a)	PD	1.9	W	
Drain power dissipati	on (t = 10 s) (Note 2b)	PD	1.0	W	
Single pulse avalancl	ne energy (Note 3)	E _{AS}	219	mJ	
Avalanche current		I _{AR}	-13	А	
Repetitive avalanche	energy Note 2a) (Note 4)	E _{AR}	0.19	mJ	
Channel temperature	1	T _{ch}	150	°C	
Storage temperature	range	T _{stg}	–55 to 150	°C	



Weight: 0.080 g (typ.)

Circuit Configuration



Note: For (Note 1), (Note 2), (Note 3) and (Note 4), please refer to the next page.

This transistor is an electrostatic sensitive device. Please handle with caution.

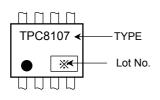
Unit: mm

TOSHIBA

Thermal Characteristics

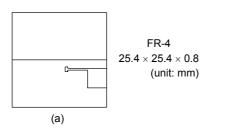
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2a)	R _{th (ch-a)}	65.8	°C/W	
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W	

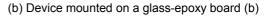
Marking (Note 5)

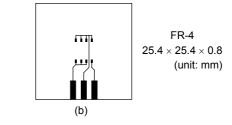


Note 1: Please use devices on condition that the channel temperature is below $150^{\circ}C$.

Note 2: (a) Device mounted on a glass-epoxy board (a)







Note 3: $V_{DD} = -24$ V, $T_{ch} = 25^{\circ}C$ (initial), L = 1.0 mH, $R_G = 25 \Omega$, $I_{AR} = -13$ A

Note 4: Repetitive rating; pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.



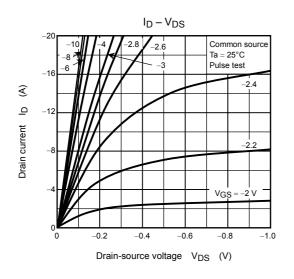
Electrical Characteristics (Ta = 25°C)

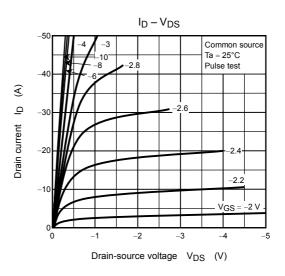
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rent	I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_		±10	μA	
Drain cut-OFF cu	irrent	I _{DSS}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			-10	μA	
Drain-source brea	akdawa valtaga	V (BR) DSS	$I_{D} = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30		_	V	
Diam-source bie	akuown vollage	V (BR) DSX	$I_D = -10$ mA, $V_{GS} = 20$ V	10 -30 -15 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -0.8 -10 15 1000 111 110 110	v			
Gate threshold vo	oltage	V _{th}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-0.8		-2.0	V	
Drain-source ON resistance		Pro (ou)	$V_{GS} = -4 \text{ V}, \text{ I}_{D} = -6.5 \text{ A}$		10	15		
		R _{DS (ON)}	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -6.5 \text{ A}$	_	5.5	7.0	mΩ	
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -6.5 \text{ A}$	15.5	31	_	S	
Input capacitance		C _{iss}	V_{DS} = –10 V, V_{GS} = 0 V, f = 1 MHz	_	5880	_	pF	
Reverse transfer capacitance		C _{rss}		_	1000	_		
Output capacitance		C _{oss}		_	1050	_		
Output capacitance	Rise time	tr	$V_{GS} \stackrel{0}{} V \stackrel{1}{} I_{D} = -6.5 \text{ A}$		11	_	- ns	
	Turn-ON time	t _{on}			22	_		
	Fall time	t _f			110			
	Turn-OFF time	t _{off}	$V_{DD}\simeq -15~V \label{eq:VDD}$ Duty \leq 1%, $t_W=10~\mu s$		395			
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq -24 \text{ V}, \text{ V}_{GS} = -10 \text{ V},$		130	_	nC	
Gate-source charge 1		Q _{gs1}	$I_{\rm D} = -13 \rm{A}$		10			
Gate-drain ("miller") charge		Q _{gd}			30			

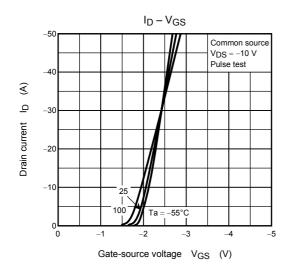
Source-Drain Ratings and Characteristics (Ta = 25°C)

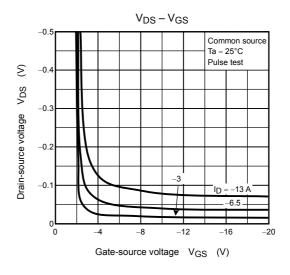
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	—	_	_	-52	А
Forward voltage (diode)			V _{DSF}	$I_{DR} = -13 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		_	1.2	V

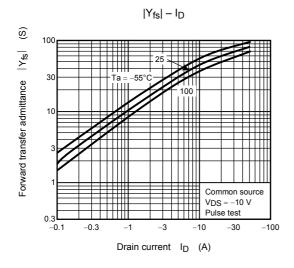
TOSHIBA



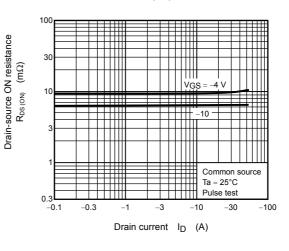




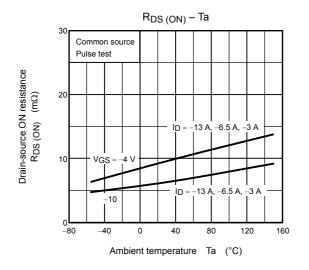


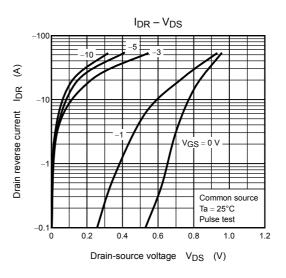


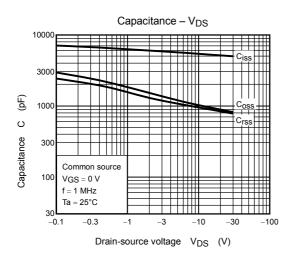
 $R_{DS(ON)} - I_D$

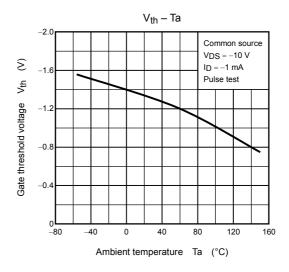


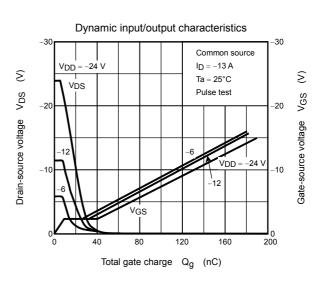
TOSHIBA

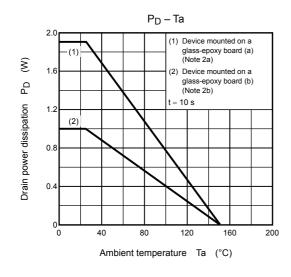


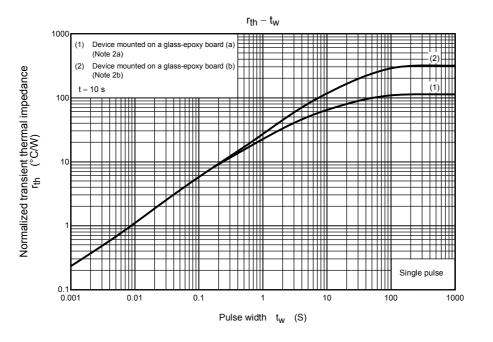




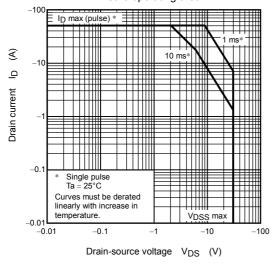








Safe operating area



RESTRICTIONS ON PRODUCT USE

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.