TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIII)

TPC8109

Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

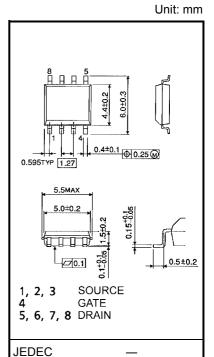
- Small footprint due to small and thin package
- Low drain-source ON resistance: RDS (ON) = 14 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 19 S$ (typ.)
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement-mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-30	V
Drain-gate voltage (R	$R_{GS} = 20 \text{ k}\Omega$	V_{DGR}	-30	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	DC (Note 1)	ΙD	-10	Α
Diam current	Pulse (Note 1)	I_{DP}	-40	^
Drain power dissipati	on $(t = 10 s)$ (Note 2a)	P_{D}	1.9	W
Drain power dissipati	on (t = 10 s) (Note 2b)	P _D	1.0	W
Single pulse avalanch	ne energy (Note 3)	E _{AS}	130	mJ
Avalanche current		I _{AR}	-10	Α
Repetitive avalanche	energy Note 2a) (Note 4)	E _{AR}	0.19	mJ
Channel temperature	!	T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C

Note: For (Note 1), (Note 2), (Note 3) and (Note 4), please refer to the next page.

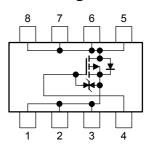
This transistor is an electrostatic sensitive device. Please handle with caution.



Weight: 0.080 g (typ.)

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Circuit Configuration

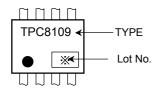


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Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W	
Thermal resistance, channel to ambient $(t=10\;s) \eqno(Note\;2b)$	R _{th (ch-a)}	125	°C/W	

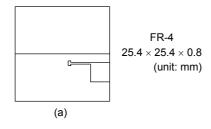
Marking (Note 5)

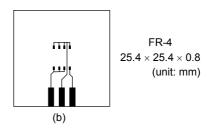


Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





Note 3: $V_{DD} = -24~V,~T_{ch} = 25^{\circ}C$ (initial), L = 1.0 mH, R_G = 25 $\Omega,~I_{AR} = -10~A$

Note 4: Repetitive rating; pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.



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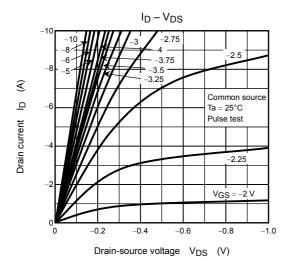
Electrical Characteristics (Ta = 25°C)

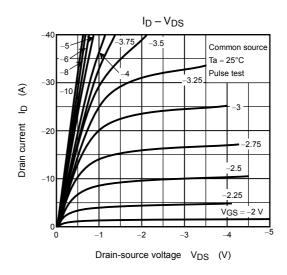
Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-OFF cu	rrent	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА
Drain-source brea	akdown voltage	V _{(BR) DSS}	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	— 24 30 — 14 20 9 19 — — 2260 —		_	V
Diam-source brea	akdown voltage	V _{(BR) DSX}	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	- ±101010		
Gate threshold vo	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	24 30		V
Drain-source ON resistance		Б	$V_{GS} = -4 \text{ V}, I_D = -5 \text{ A}$	_	24	30	mΩ
Diain-source ON	resistance	IDSS	$V_{GS} = -10 \text{ V}, I_D = -5 \text{ A}$	_	14	20	1115.2
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -5 \text{ A}$	9	19	_	S
Input capacitance)	C _{iss}		_	2260	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	290	_	pF
Output capacitance		C _{oss}		_	350	_	
	Rise time	t _r	0 V J C ln = -5 A	_	5	_	_
Switching time	Turn-ON time	t _{on}	V _{GS} V _{OUT}	_	13	±10 -102.0 30	
	34	_	ns				
	Turn-OFF time	t _{off}		_	143	_	
Total gate charge (gate-source plus	otal gate charge gate-source plus gate-drain)		$V_{DD} \simeq -24 \text{ V}, V_{GS} = -10 \text{ V},$ $I_{D} = -10 \text{ A}$		45		nC
Gate-source charge 1		Q _{gs1}		_	6.5	_	
Gate-drain ("mille	r") charge	Q _{gd}		_	10	_	

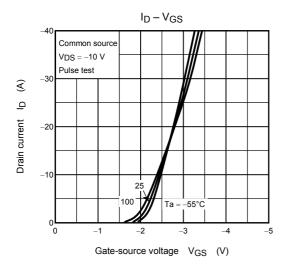
Source-Drain Ratings and Characteristics (Ta = 25°C)

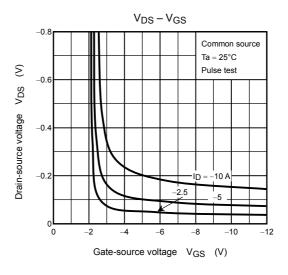
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	-40	Α
Forward voltage (diode)			V_{DSF}	$I_{DR} = -11 \text{ A}, V_{GS} = 0 \text{ V}$			1.2	V

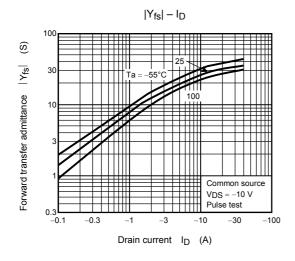
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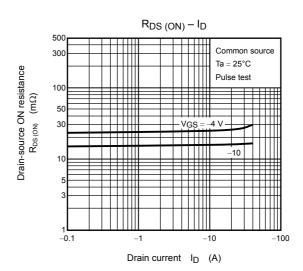


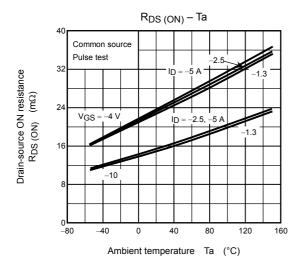


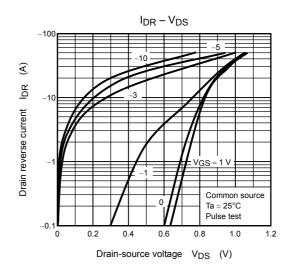


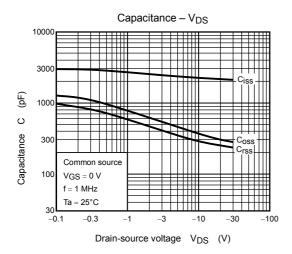


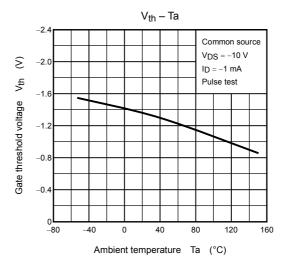


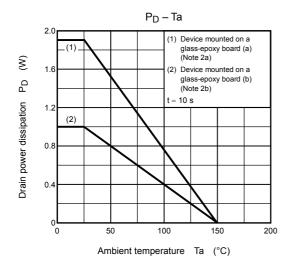


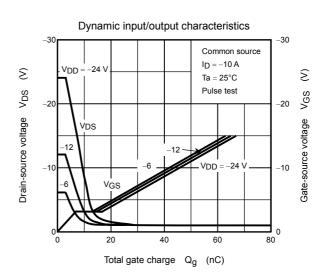




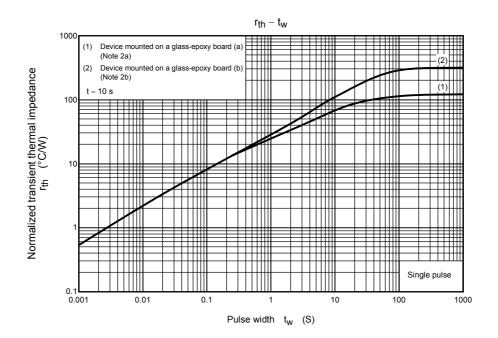


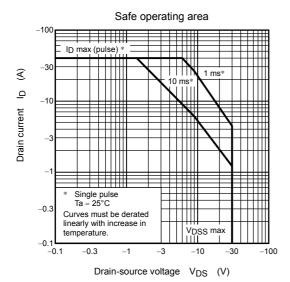






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