XN01871 (XN1871)

Silicon N-channel junction FET

For amplification of the low frequency

Features

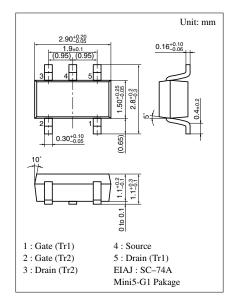
- Two elements incorporated into one package. (Soure-coupled FETs)
- Reduction of the mounting area and assembly cost by one half.

Basic Part Number of Element

• $2SK0198(2SK198) \times 2$ elements

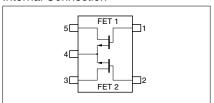
Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Ratings	Unit	
Rating of element	Drain to source voltage	V_{DSX}	30	V	
	Gate to drain voltage	V_{GDO}	-30	V	
	Drain current	I_D	20	mA	
	Gate current	I_G	10	mA	
Overall	Total power dissipation	P_{T}	300	mW	
	Channel temperature	T_{ch}	150	°C	
	Storage temperature	T_{stg}	-55 to +150	°C	



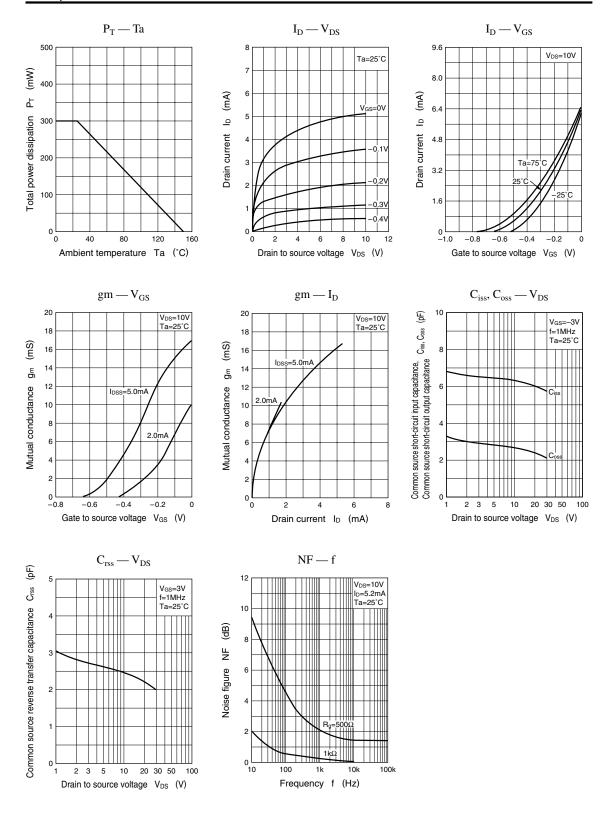
Marking Symbol: 5T

Internal Connection



Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain current	I _{DSS}	$V_{DS} = 10V, V_{GS} = 0$	0.5		12	mA
Gate cutoff current	I_{GSS}	$V_{GS} = -30V, V_{DS} = 0$			-100	nA
Gate to source cutoff voltage	V _{GSC}	$V_{DS} = 10V, I_D = 10\mu A$	- 0.1		-1.5	V
Mutual conductance	gm	$V_{DS} = 10V, I_D = 0.5 \text{mA}, f = 1 \text{MHz}$	4			mS
Mutual conductance	gm	$V_{DS} = 10V, V_{GS} = 0V, f = 1MHz$	4	12		mS
Common source short-circuit input capacitance	C _{iss}	$V_{DS} = 10V, V_{GS} = 0V, f = 1MHz$		14		pF
Common source reverse transfer capacitance	C _{rss}	$V_{DS} = 10V, V_{GS} = 0V, f = 1MHz$		3.5		pF
Noise voltage	NV	$V_{DS} = 30V$, $I_D = 1mA$, $G_V = 80dB$ $Rg = 100k\Omega$, Function = FLAT		60		mV



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