

# SCHEMATIC DIA

1 2 3 4 5 6

A

B

C

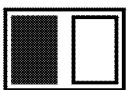
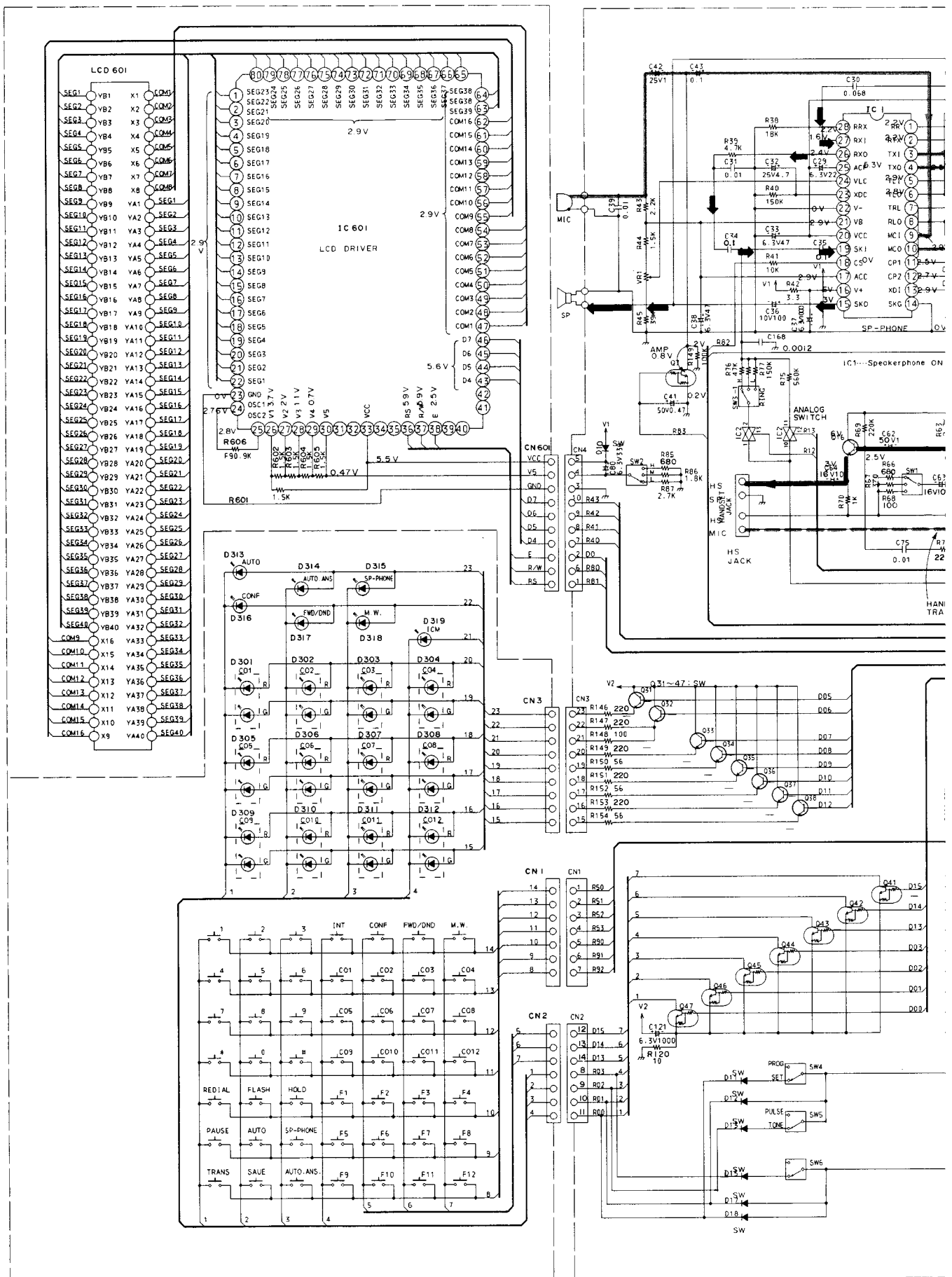
D

E

F

G

H



# ATIC DIAGRAM

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12

The diagram illustrates the internal circuitry of a mobile phone system, divided into sections 7 through 12. Key components include:

- IC1 (Speakerphone ON mode):** Located in section 7, it features pins for RX, TX, V, VCC, and SKD. It is connected to various resistors (R1-R100) and capacitors (C1-C100).
- IC5 (CPU):** A central component in section 11 with pins for CPU (D1-D15), OSC1, OSC2, TEST, RESET, RA, and VCC. It is connected to a complex network of resistors and capacitors.
- Power Supply:** Multiple voltage rails are shown, including 6.0V, 5.0V, 3.0V, 1.9V, and 0.5V. A 6.0V regulator (IC5) is shown in section 11.
- Control and Logic:** NAND gates (IC4), an OP AMP (IC3), and a RECTIFIER (D1) are used for signal processing and power regulation.
- Connectors:** SP-PHONE and TEL JACK connectors are shown at the top and right edges.
- Relays:** REL1 and REL2 are used for switching functions, controlled by the CPU and other logic.

SW: SWITCHING